
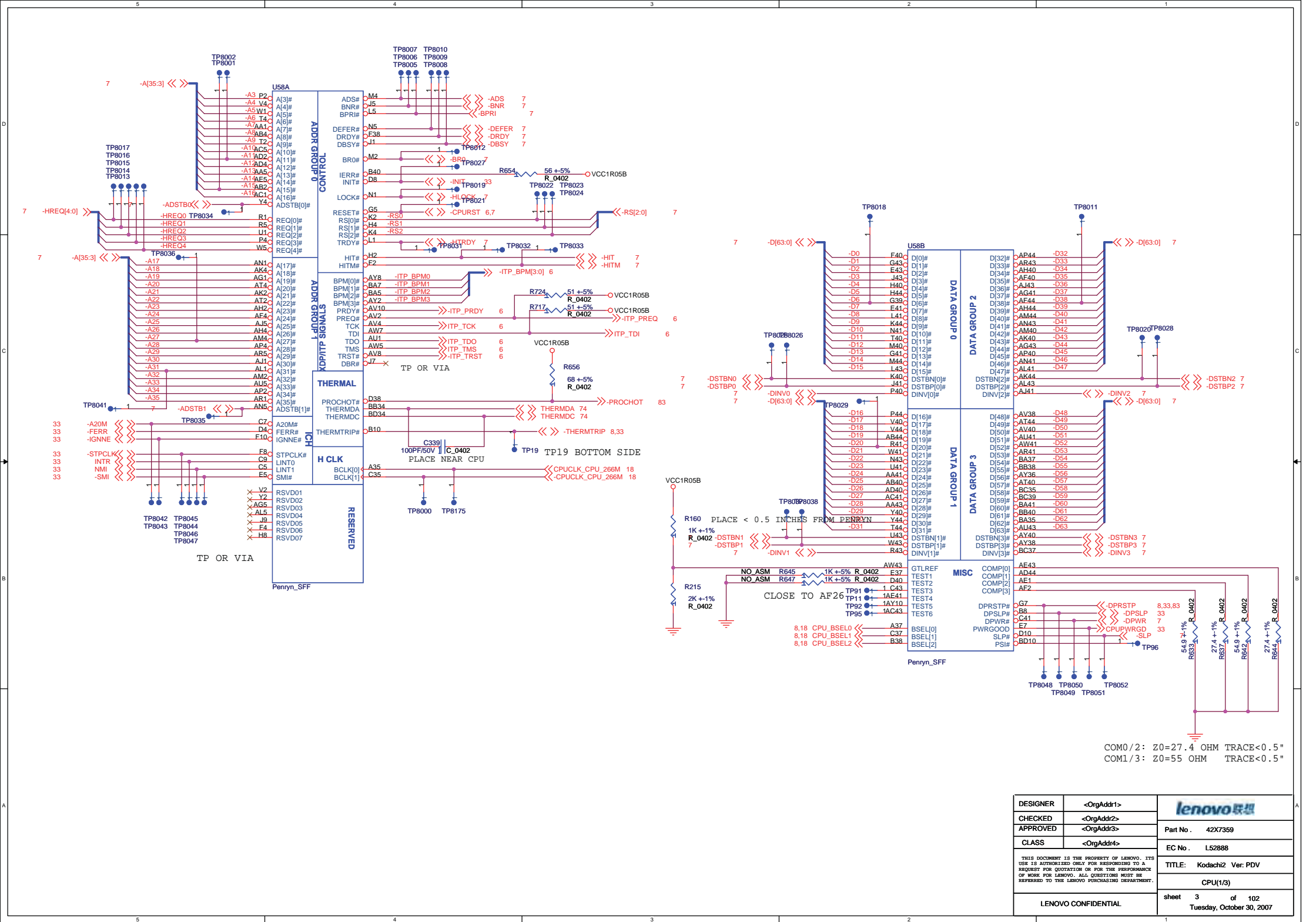
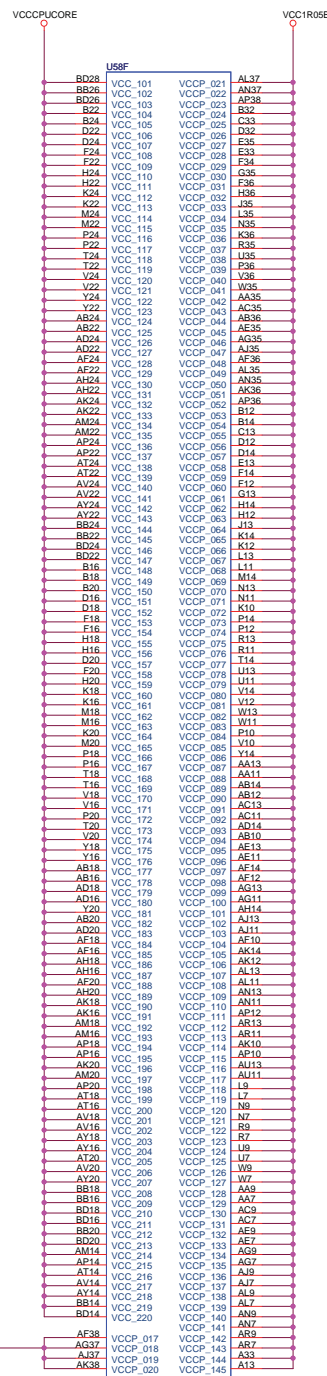
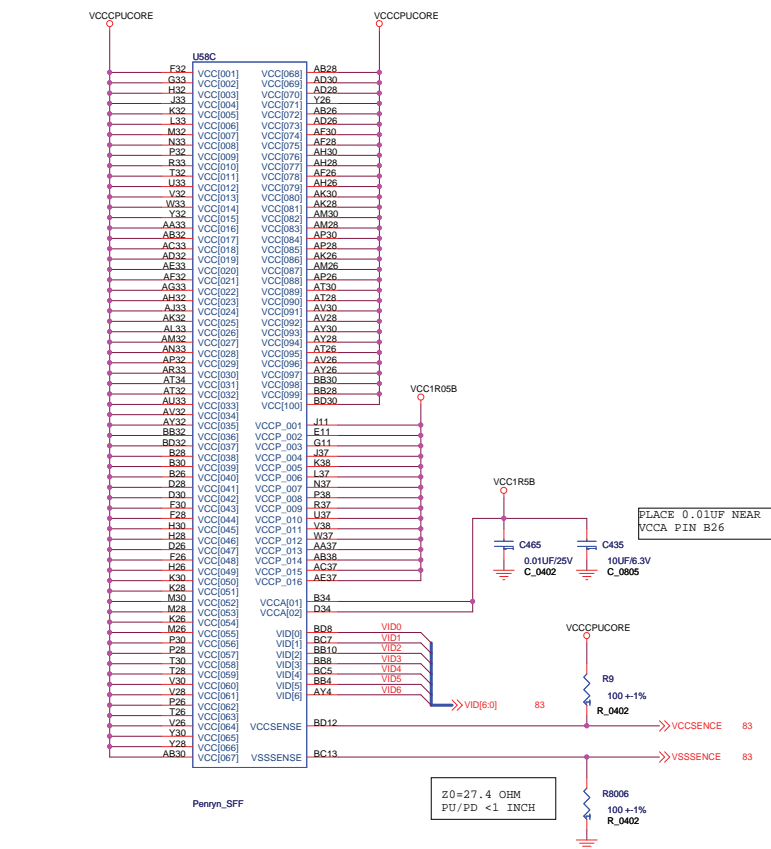



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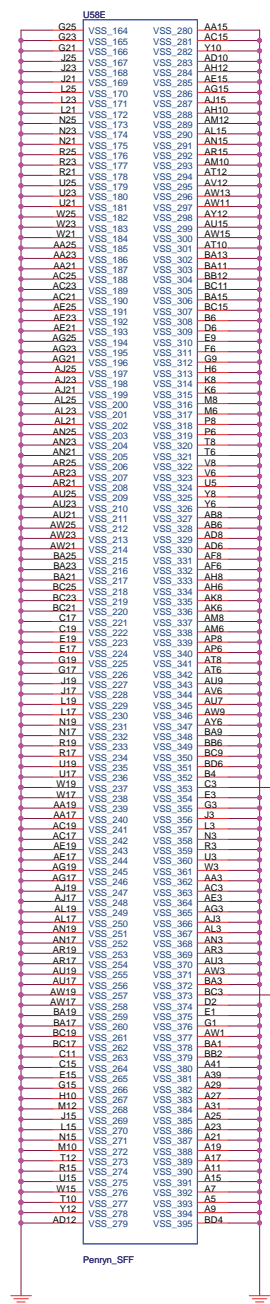
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LENOVO CONFIDENTIAL		CPU(2/3)
		sheet 2 of 102 Tuesday, October 30, 2007

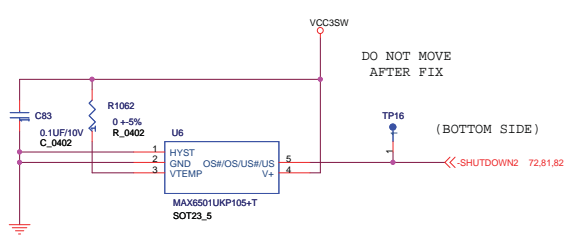
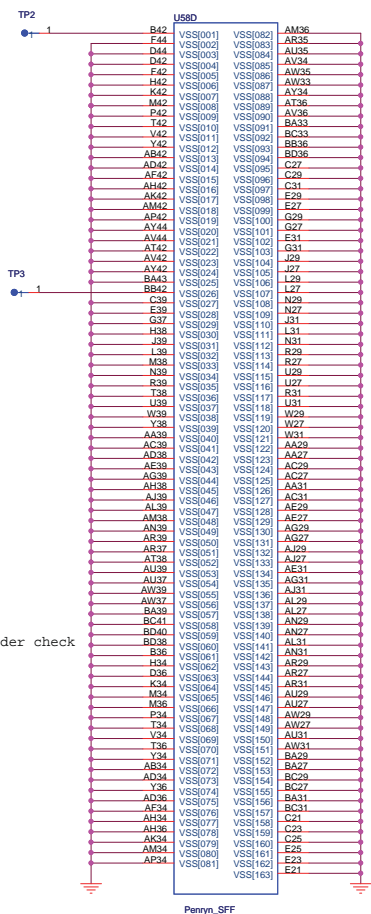




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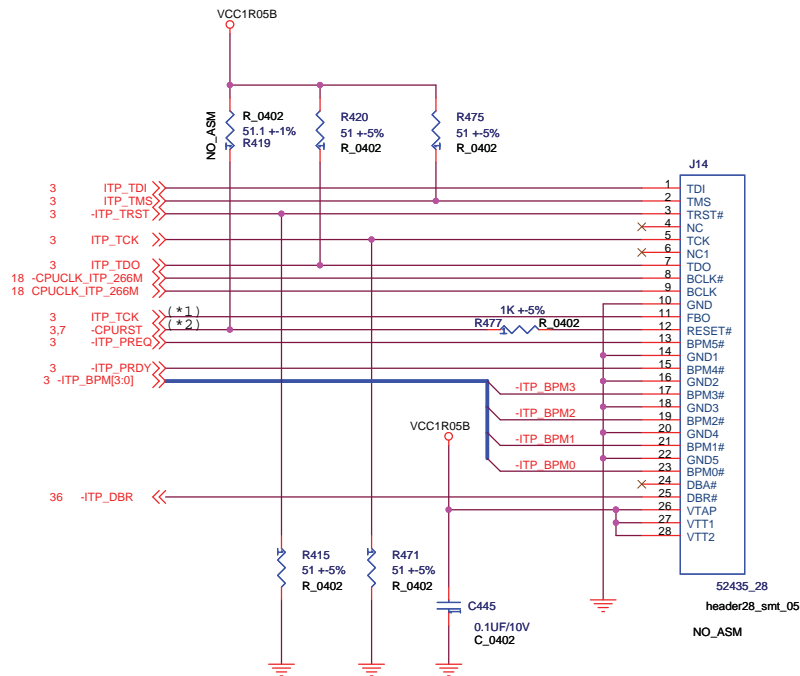


Add four corner test point for solder check



TABLE

U1	R1061
NS LM26	NOASM
MAXIM MAX6519	NOASM
MAXIM MAX6501	ASM
ADI ADT6501	ASM




TABLE

REF DES	FOR ITP700FLEX	FOR ITP-->XDP
J14	NO_ASM-->ASM	NO_ASM-->ASM
C445	ASM(NO CHANGE)	ASM(NO CHANGE)
R717	ASM-->150 5% ASM	ASM(NO CHANGE)
R477	ASM-->22.6 1% ASM	ASM(NO CHANGE)
R415	ASM(NO CHANGE)	ASM(NO CHANGE)
R419	NO_ASM-->ASM	NO_ASM(NO CHANGE)
R420	ASM(NO CHANGE)	ASM(NO CHANGE)
R471	ASM-->27.4 1% ASM	ASM(NO CHANGE)
R475	ASM-->39.2 1% ASM	ASM(NO CHANGE)

NOTES: C445 and R447:"ASM" before SIT, "NO_ASM" from SIT.
J14 "ASM" for PRE_DV only

- (*1) TCK SIGNAL IS BRANCHED AT Penryn PIN
(*2) -CPURST SIGNAL IS BRANCHED AT Cantiga PIN

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		EC No . L52888
		TITLE: Kodachi2 Ver: PDV
		ITP CONNECTOR
LENOVO CONFIDENTIAL		sheet 6 of 102 Tuesday, October 30, 2007

NEED TP OR VIA FOR THESE PINS

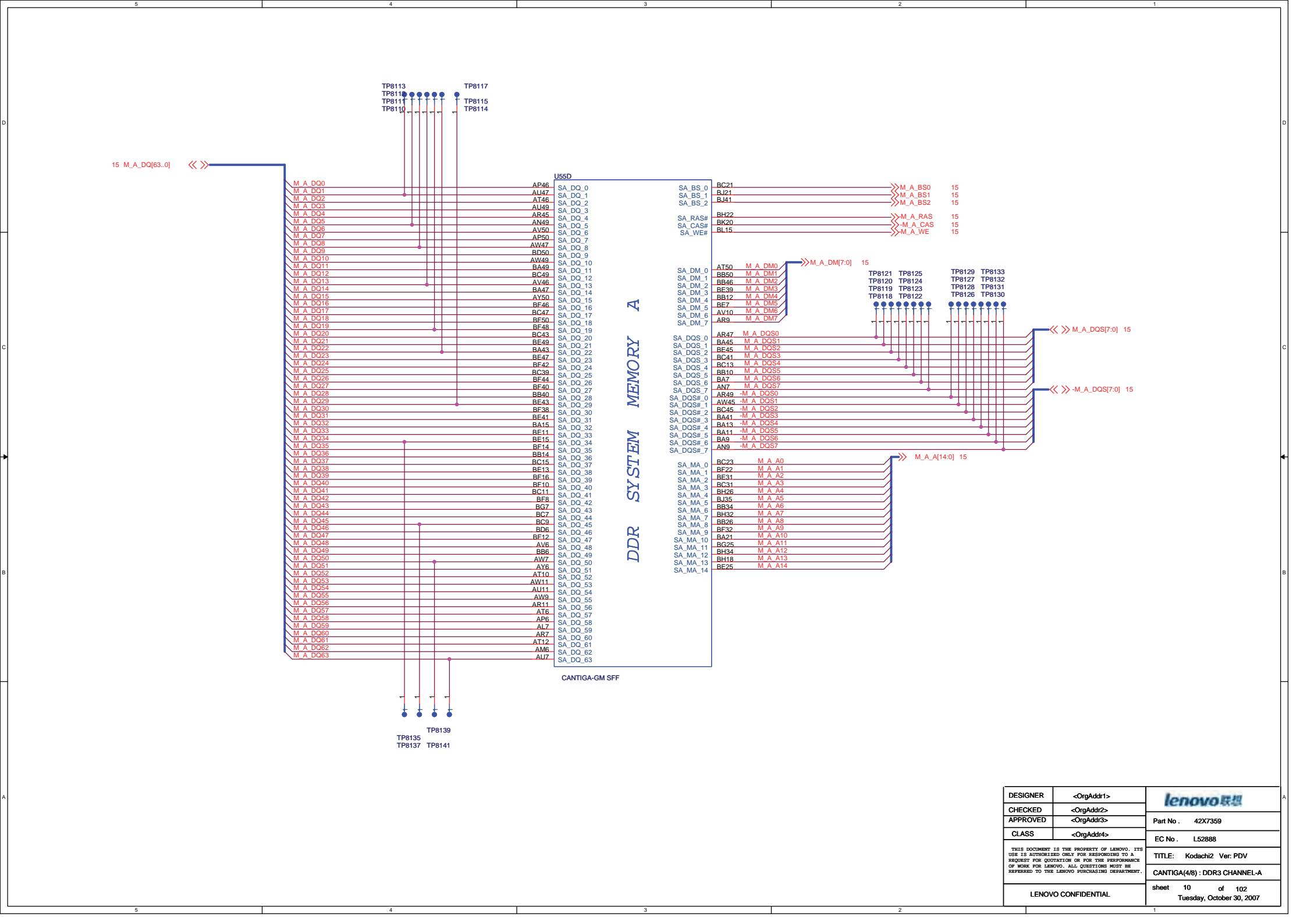
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TP OR VIA

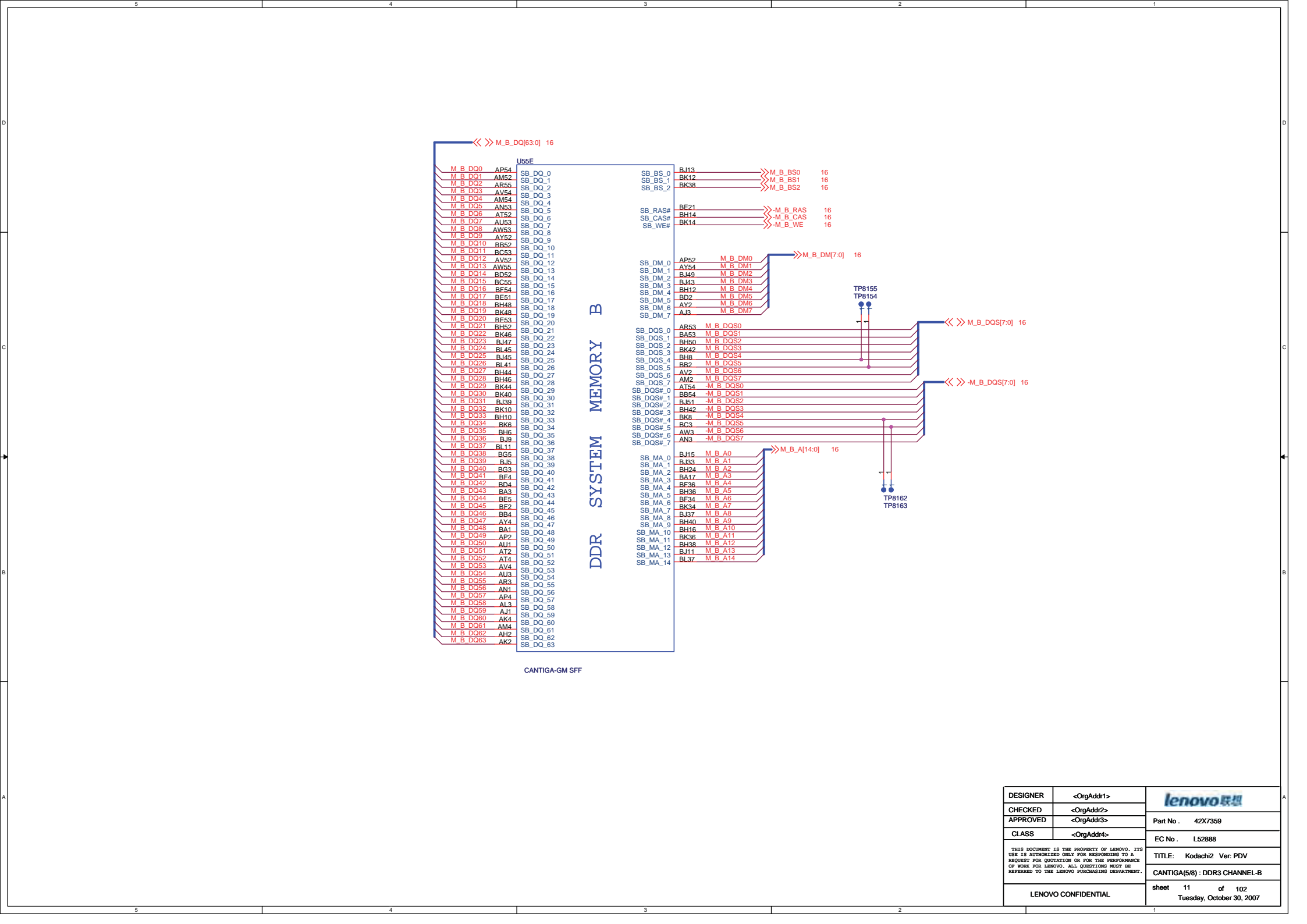
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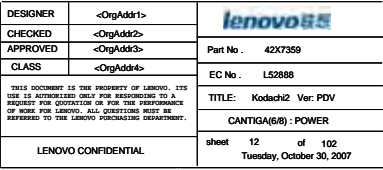
LENOVO CONFIDENTIAL	sheet 8 of 102 Tuesday, October 30, 2007
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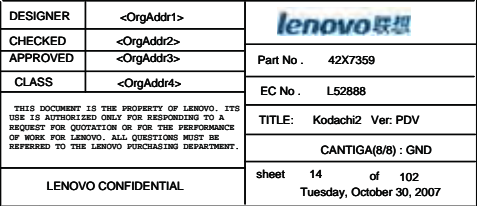


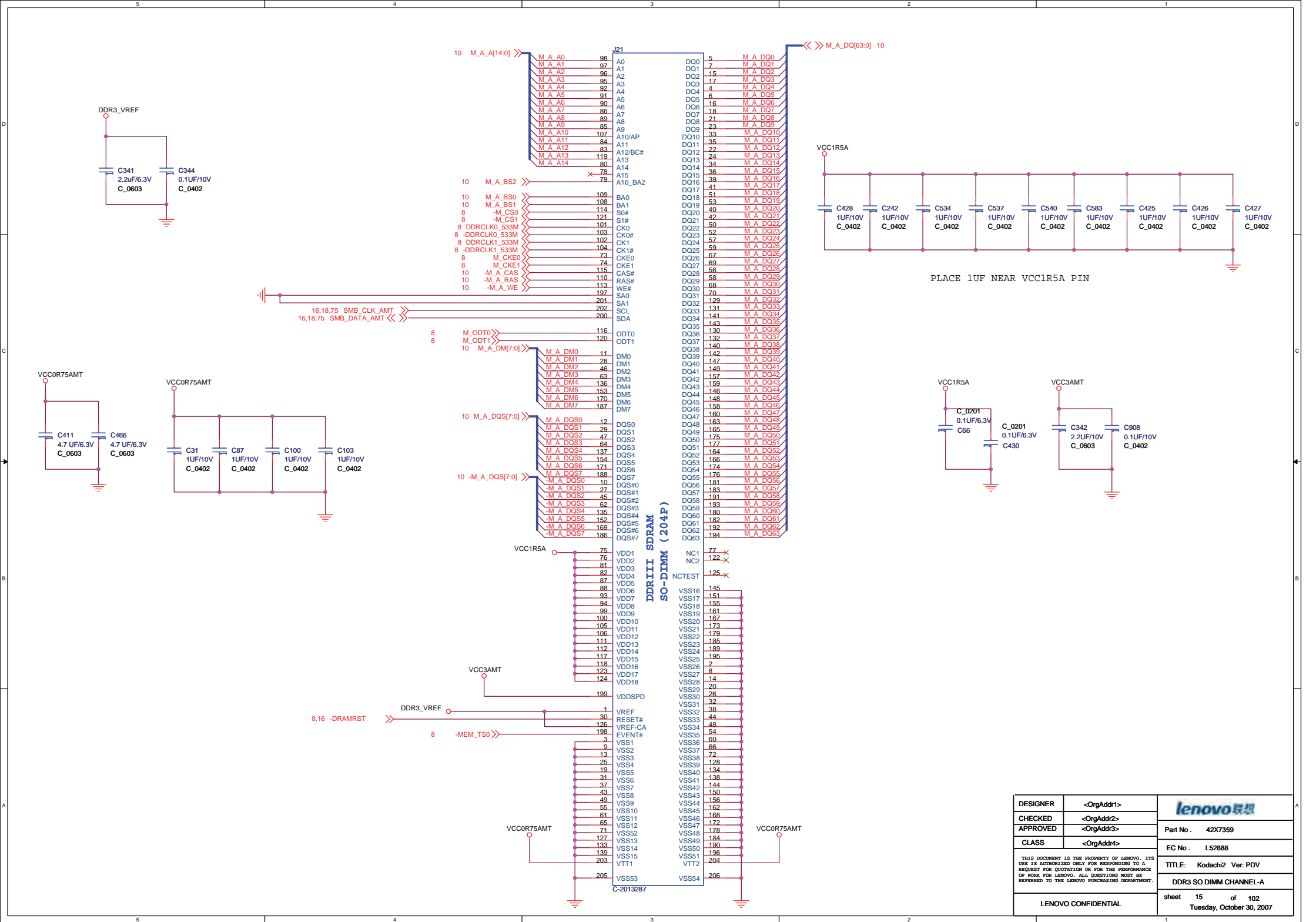
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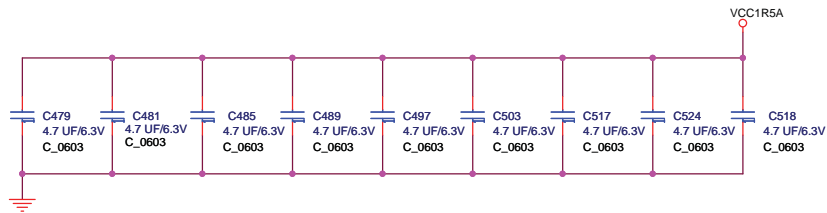
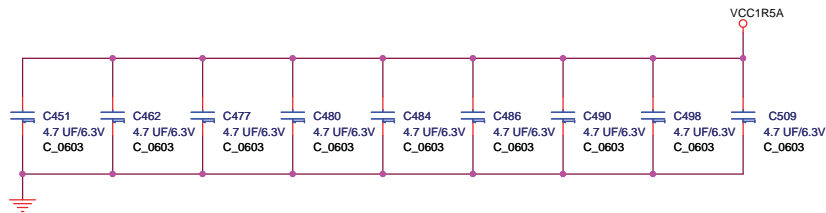
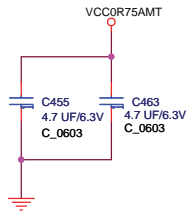


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LENOVO CONFIDENTIAL		sheet 25 of 102 Tuesday, October 30, 2007

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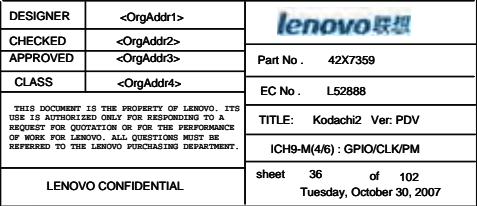
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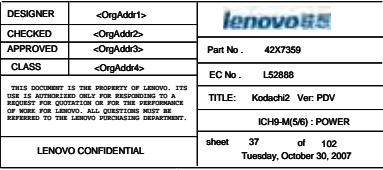
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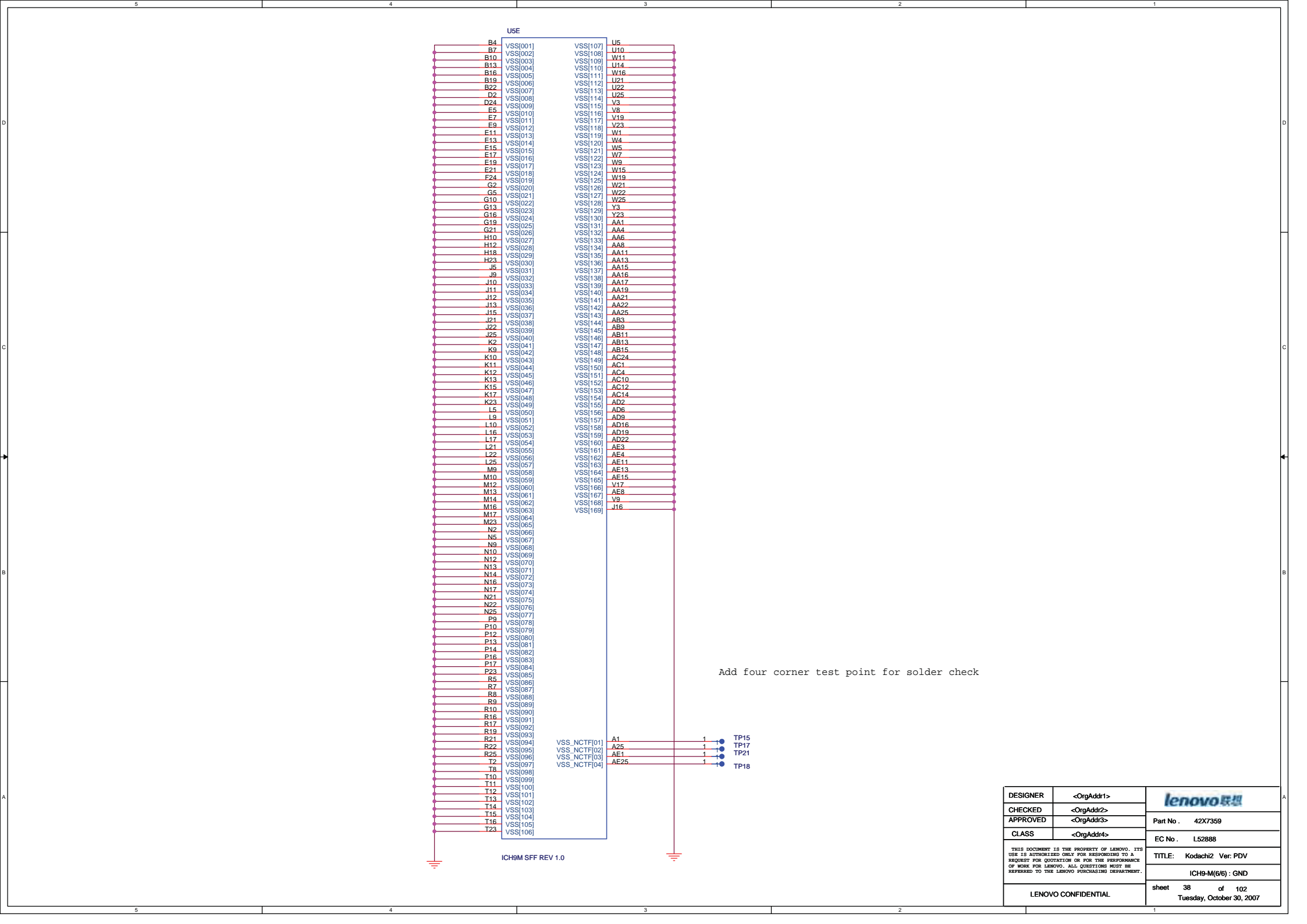
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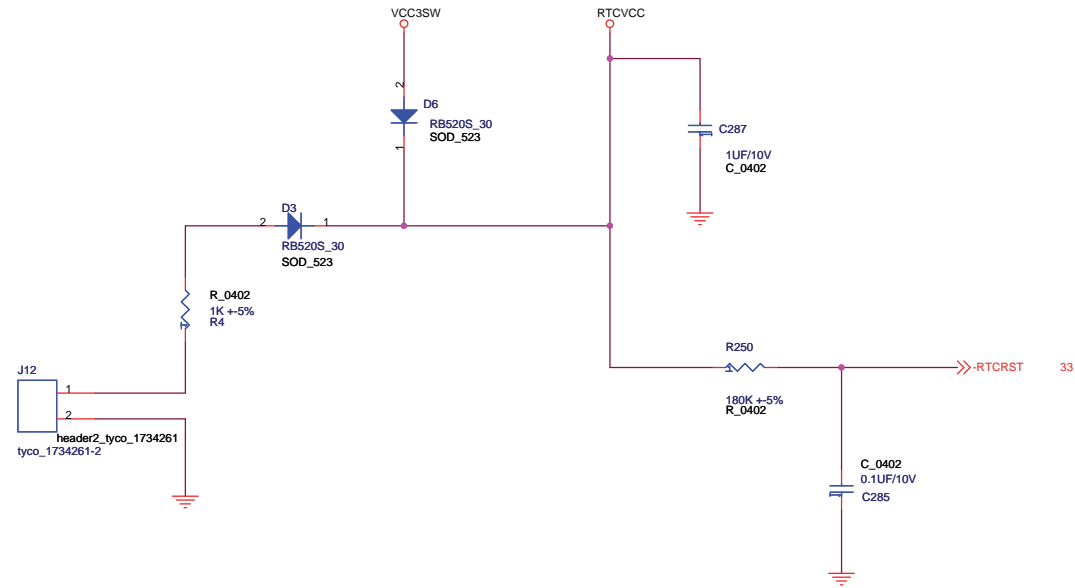
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LENOVO CONFIDENTIAL		sheet 30 of 102 Tuesday, October 30, 2007



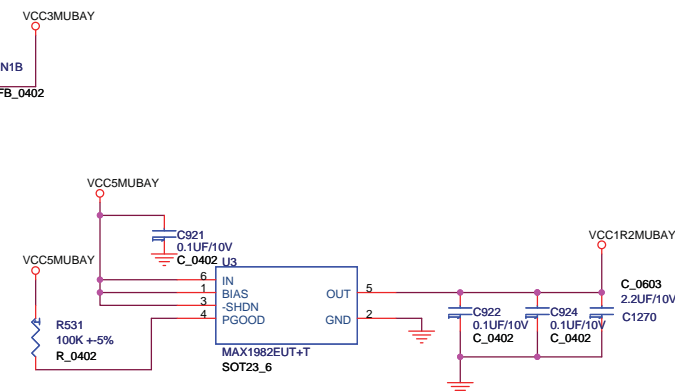




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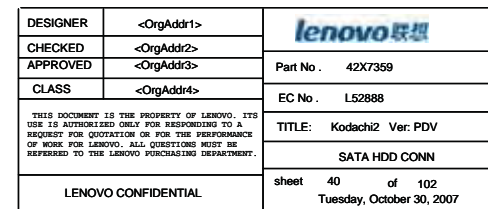


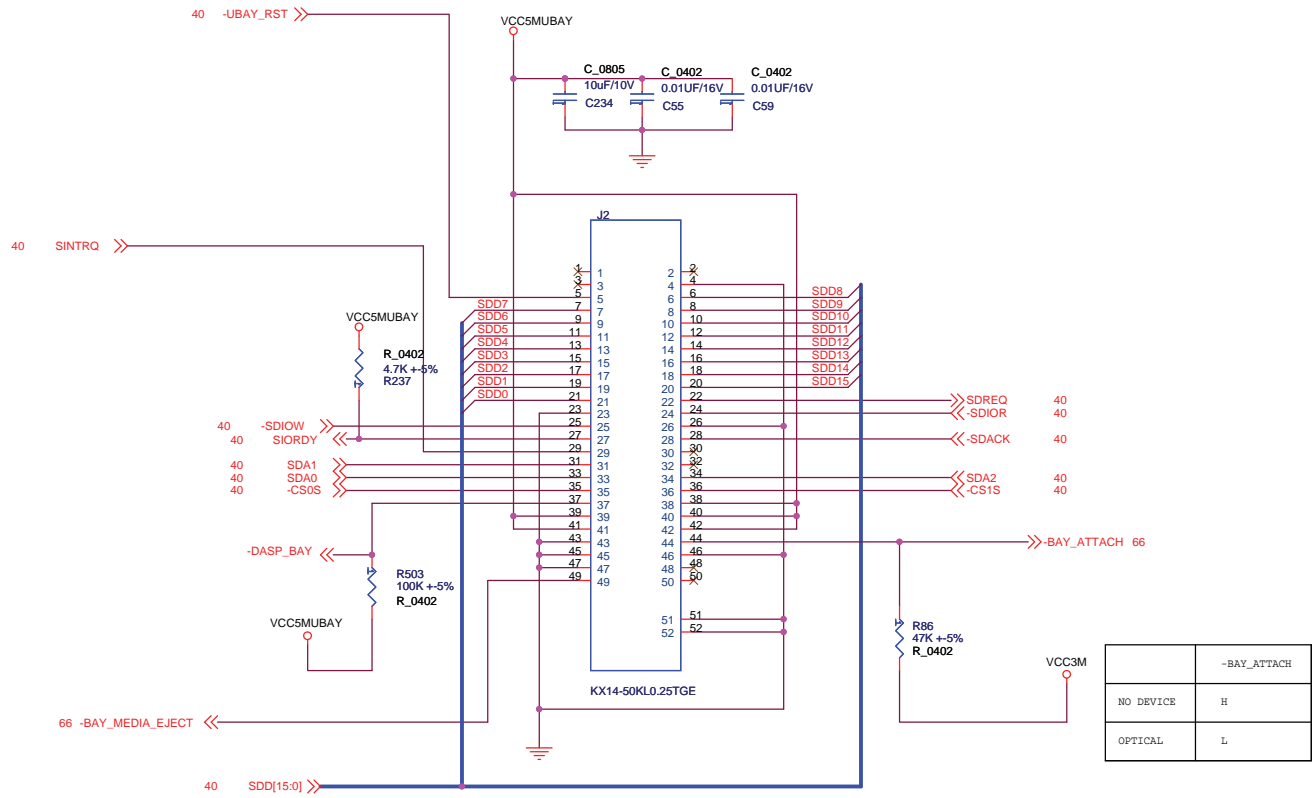
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0: Disable the output to the ATA bus.
1: Enable the output to the ATA bus. *
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001 : DEVICE_MODE_MAX_UDMA_133MB/s
010 : DEVICE_MODE_MAX_UDMA_150MB/s
011 : PHY_DEBUG_MODE

100:HOST_MODE_MAX_UDMA_100MB/s
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111:ATE SCAN MODE
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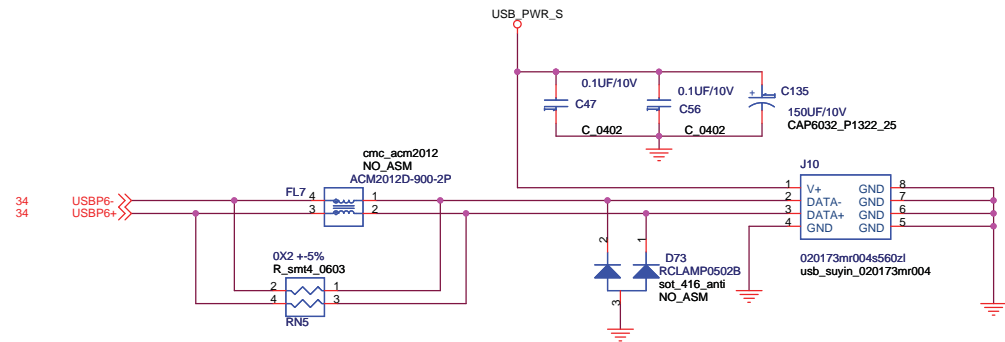
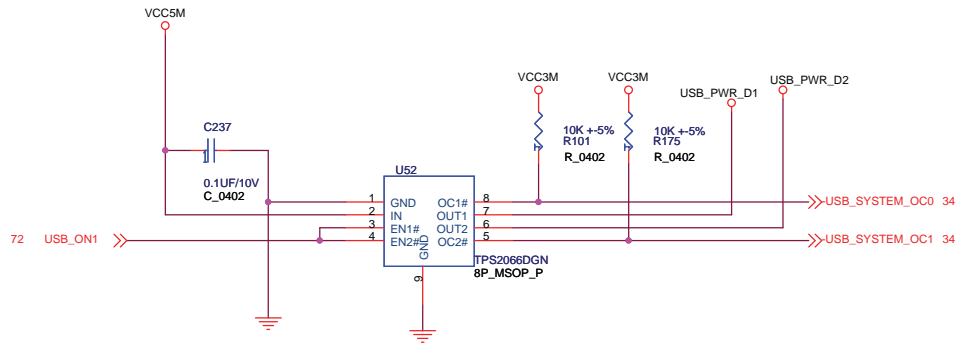
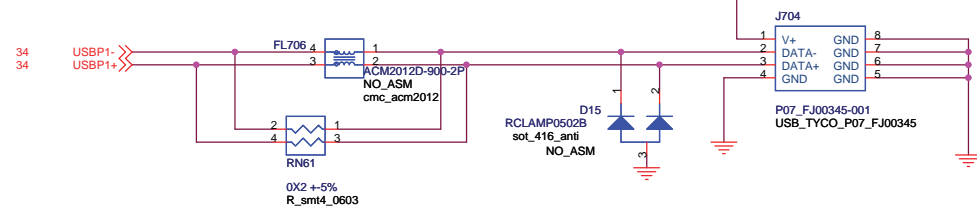
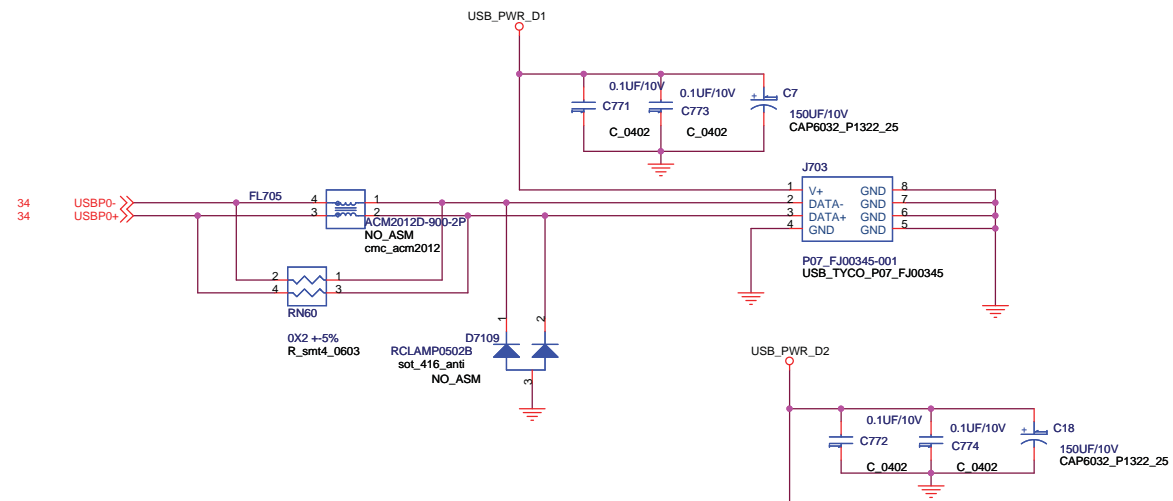
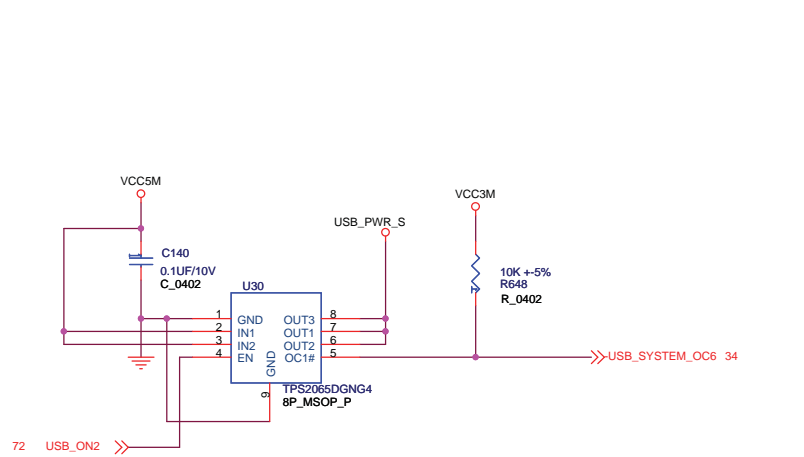
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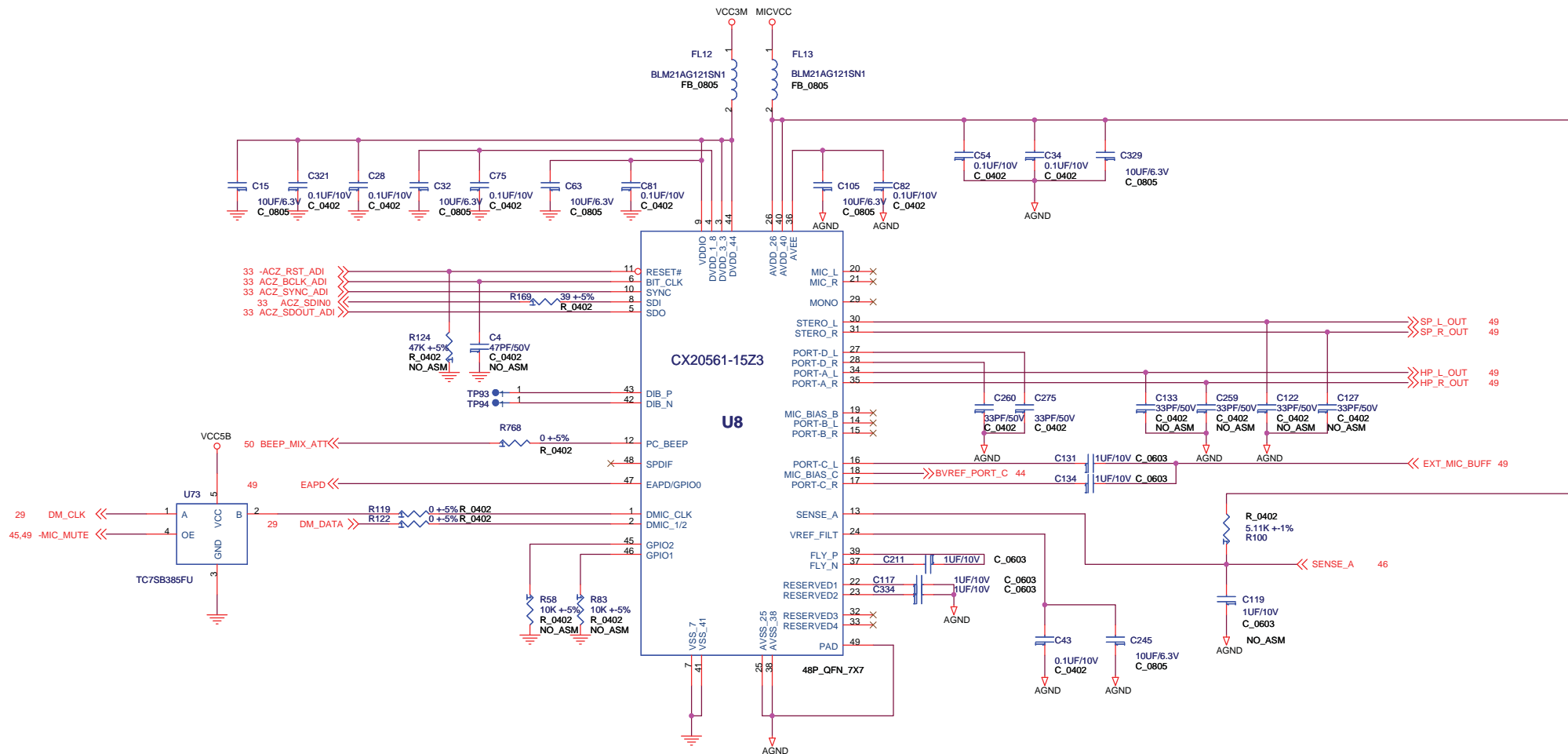


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NO DEVICE	H
OPTICAL	L

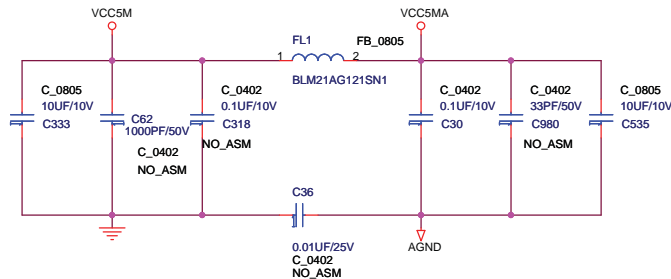
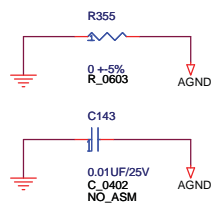
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LENOVO CONFIDENTIAL		SATA BAY I/F CONN
		sheet 41 of 102 Tuesday, October 30, 2007




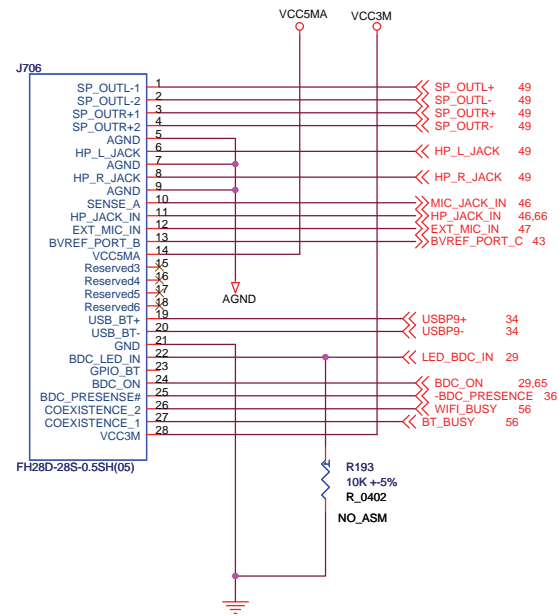
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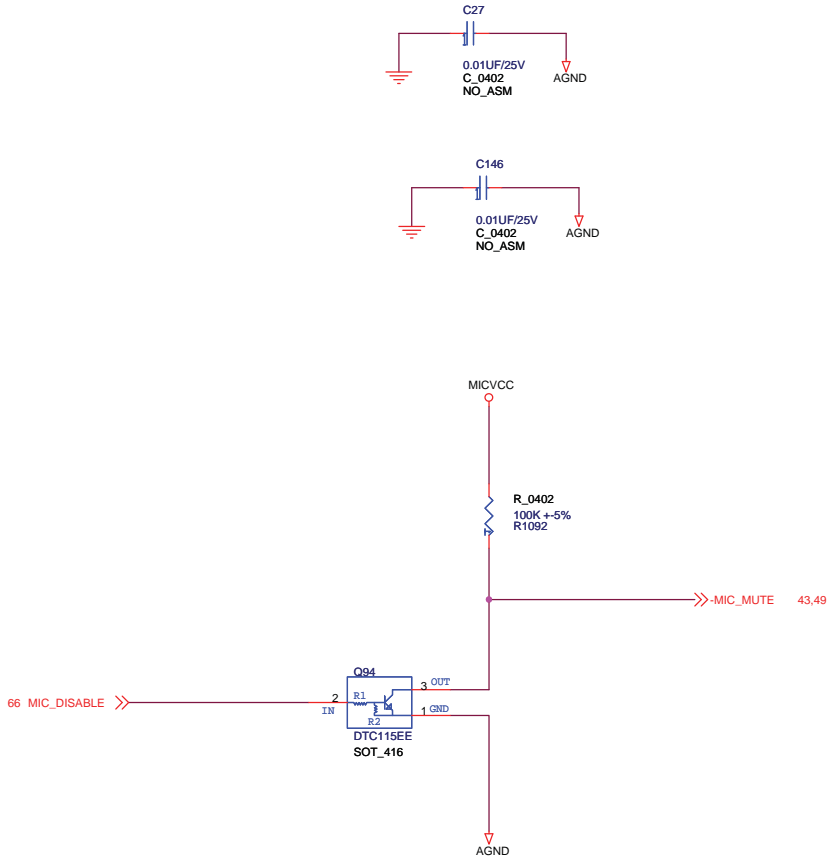
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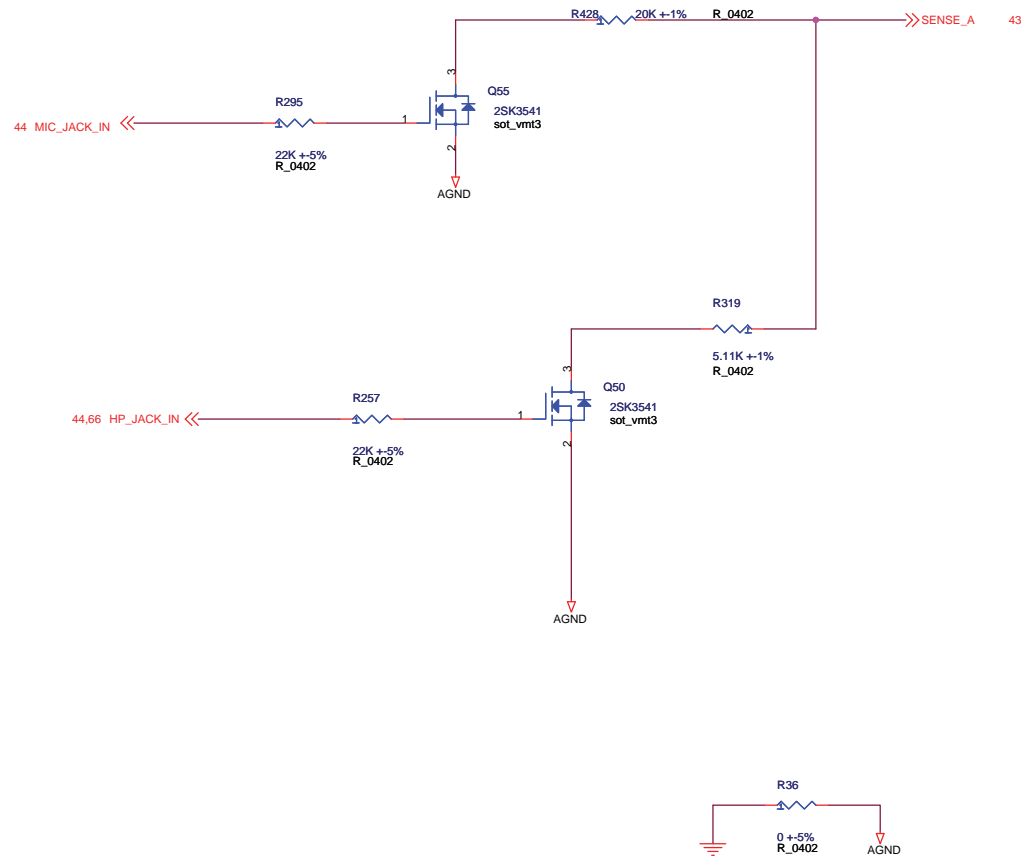
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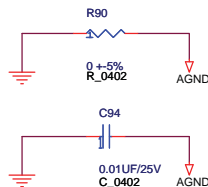
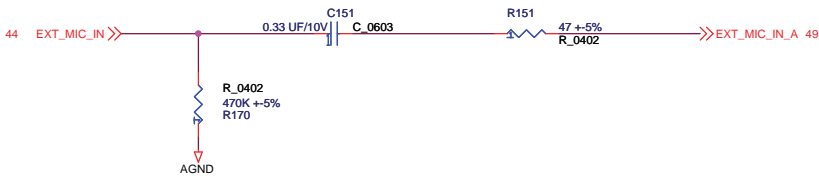
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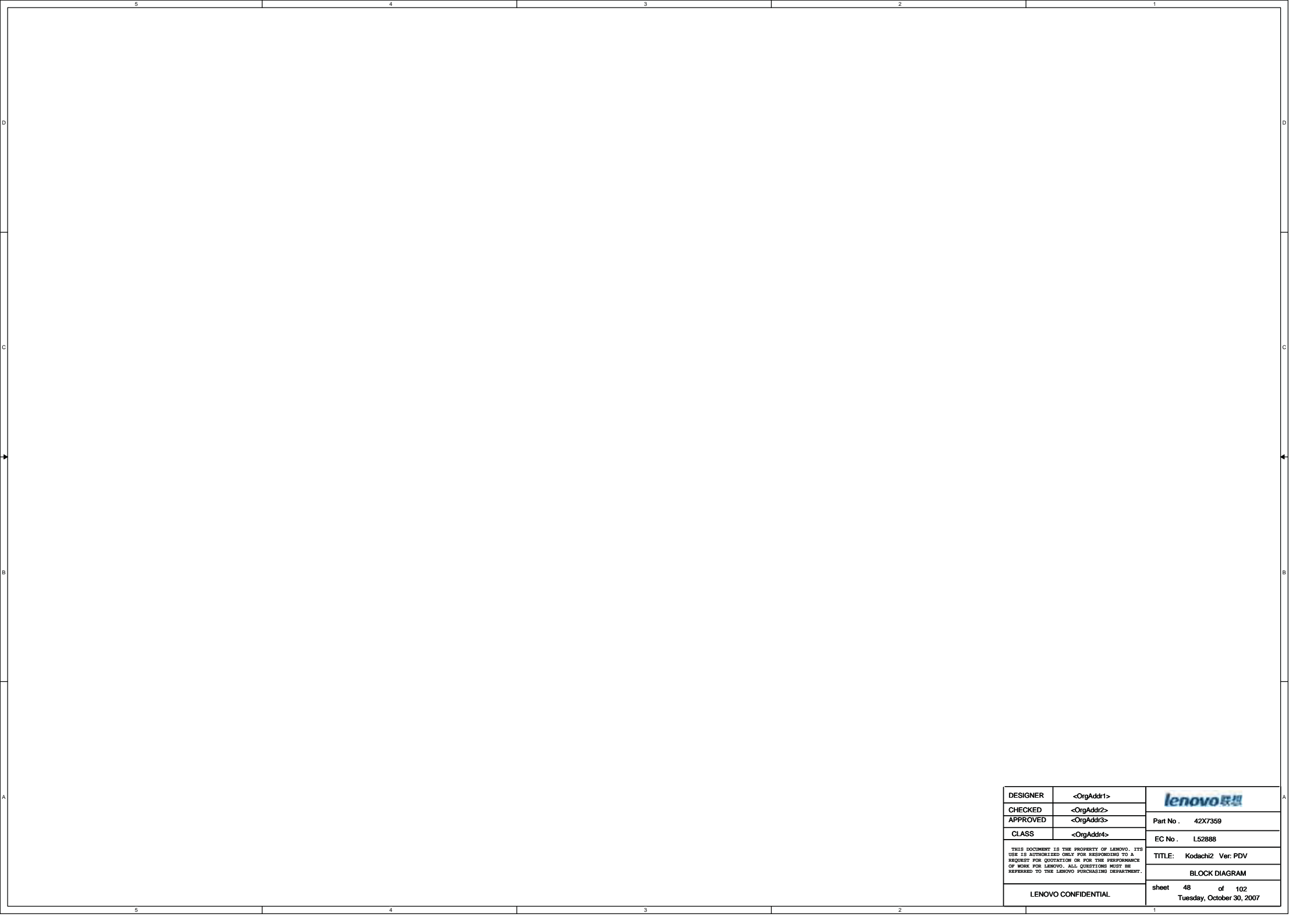
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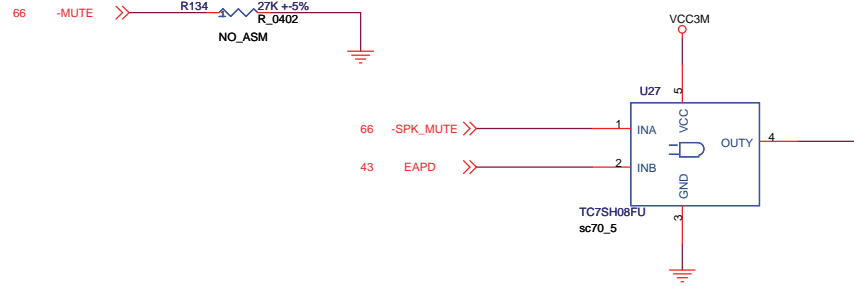
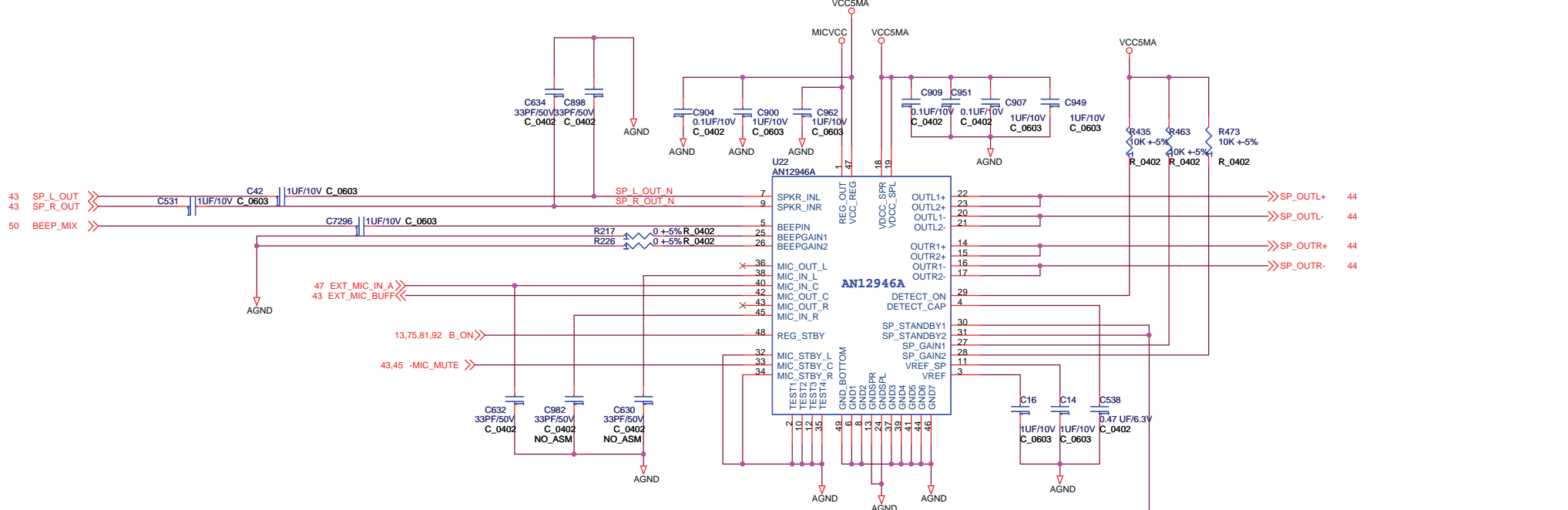
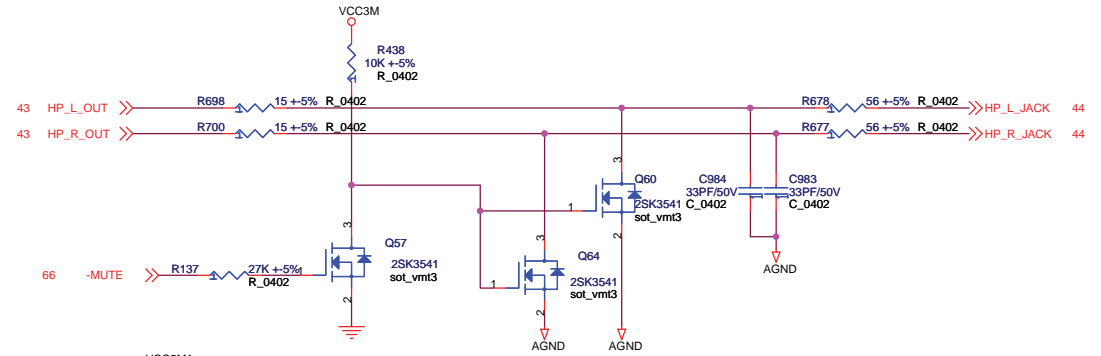
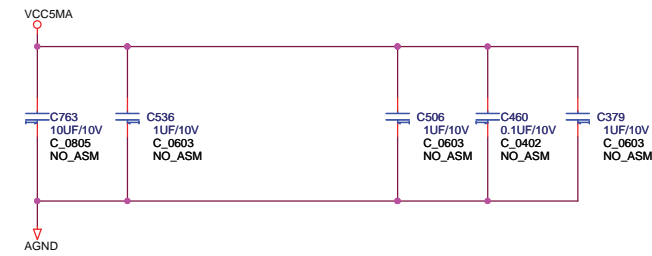
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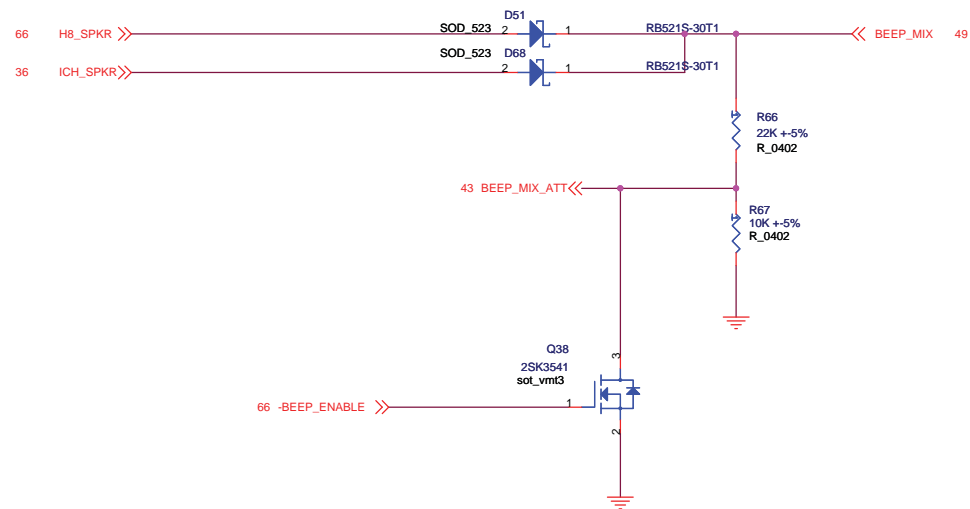
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LENOVO CONFIDENTIAL		sheet 48 of 102 Tuesday, October 30, 2007




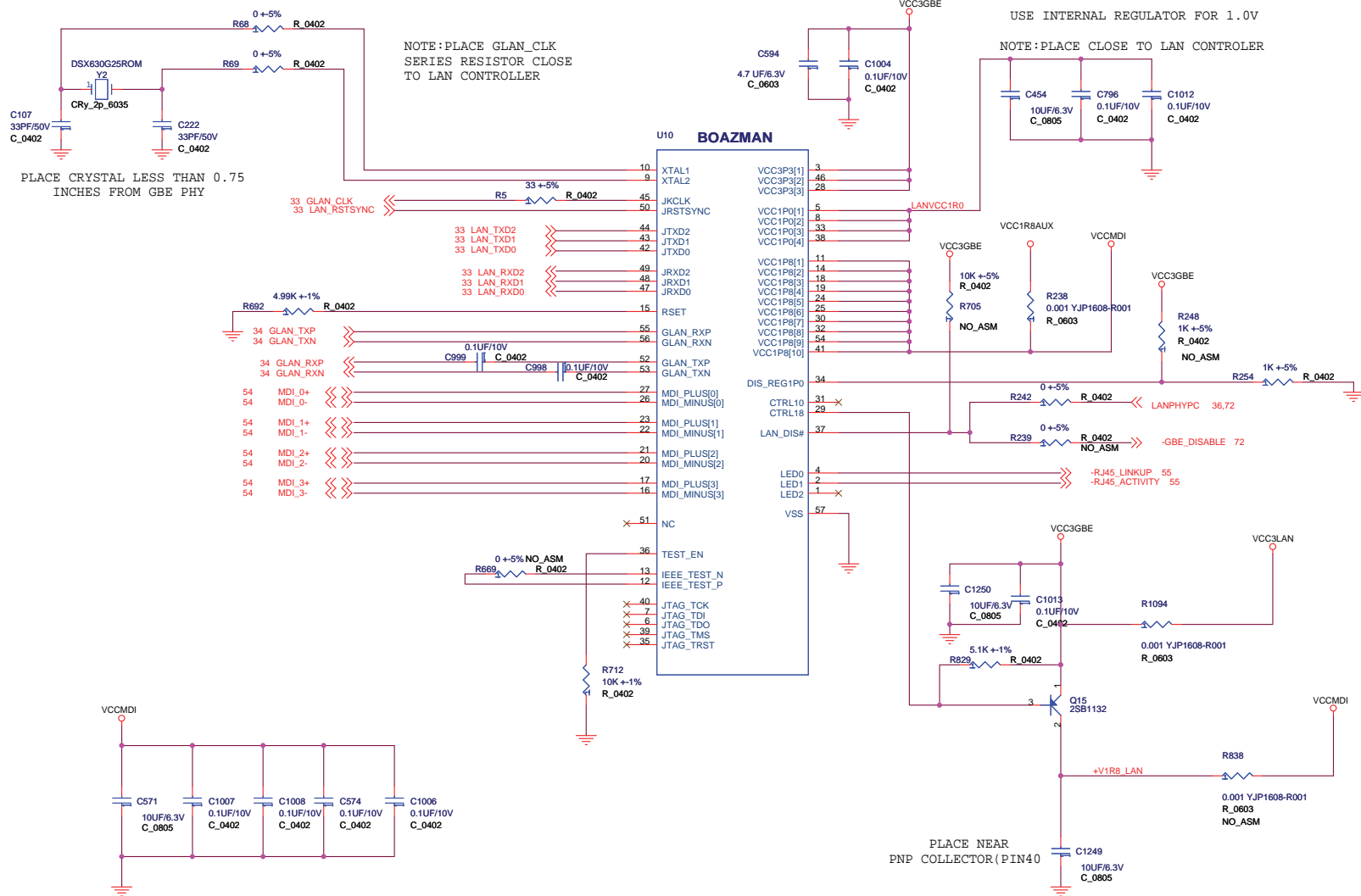
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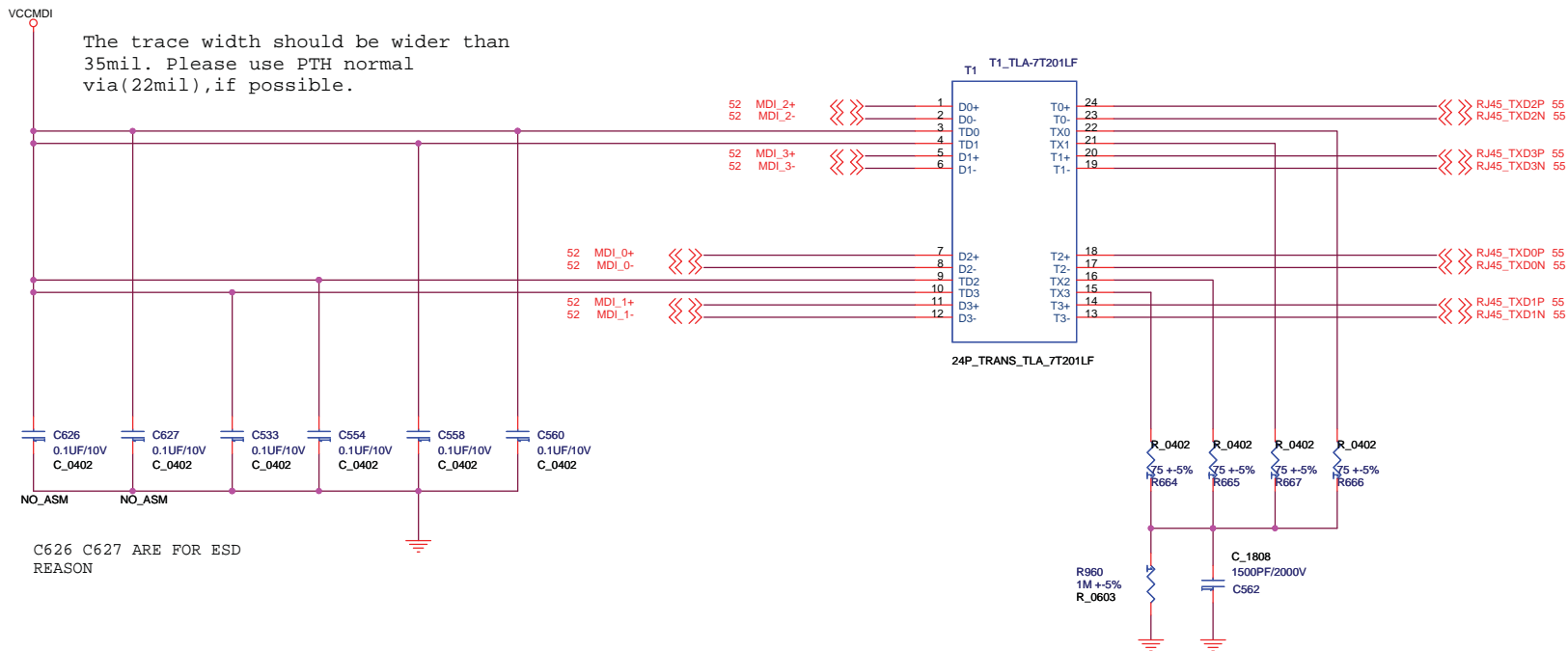
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LENOVO CONFIDENTIAL		sheet 51 of 102 Tuesday, October 30, 2007




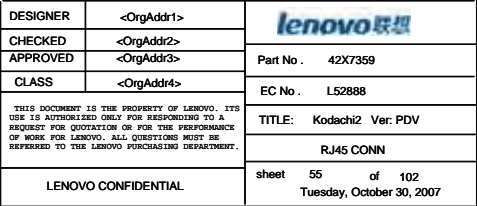
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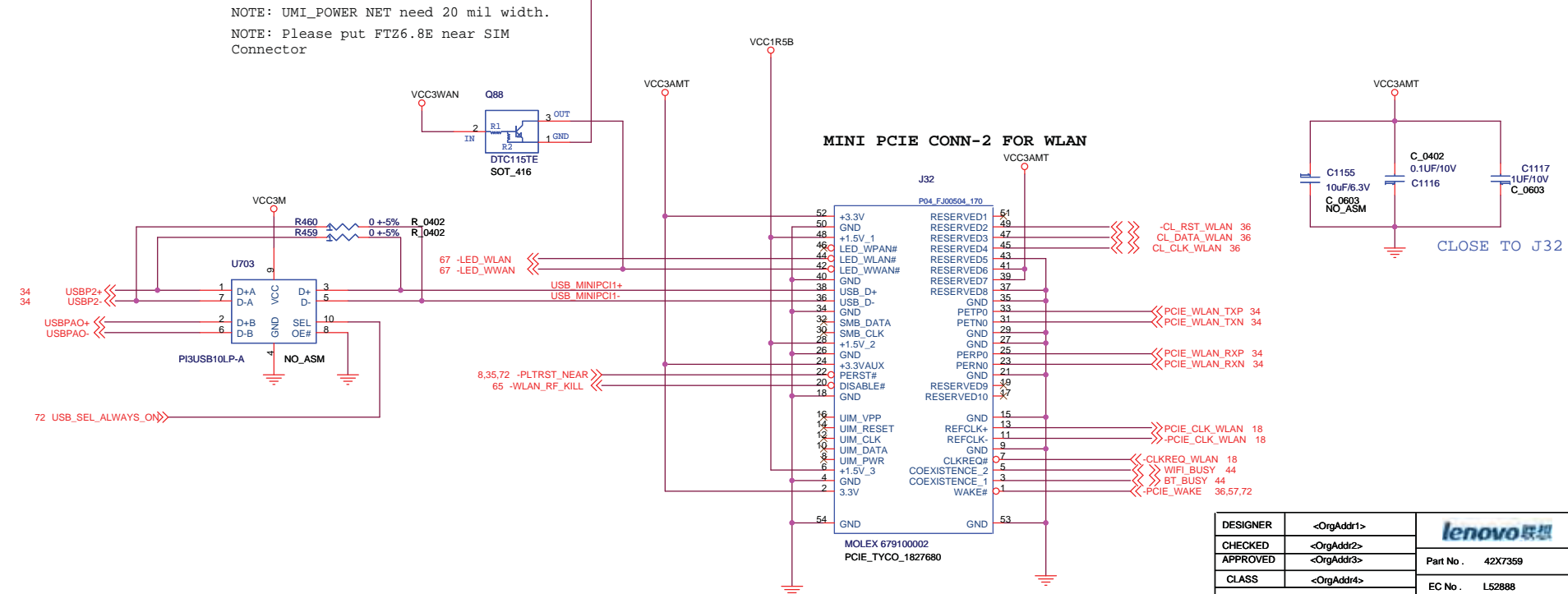
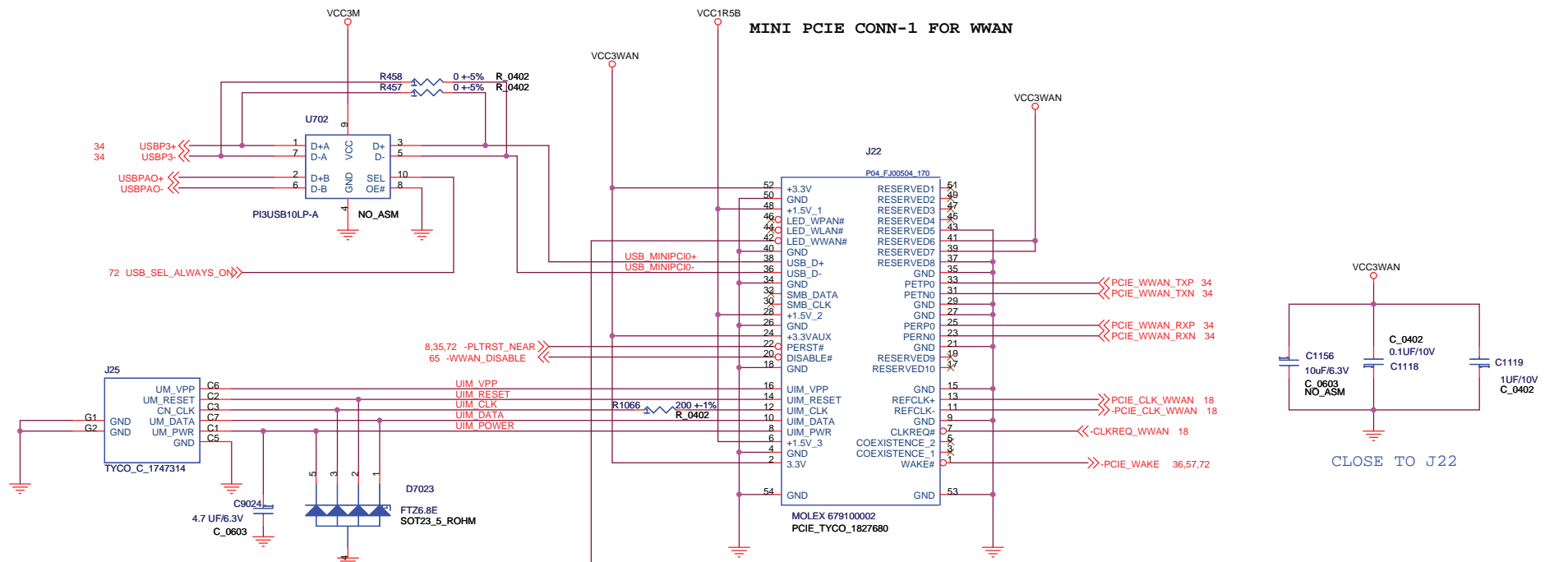
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LENOVO CONFIDENTIAL		sheet 53 of 102 Tuesday, October 30, 2007




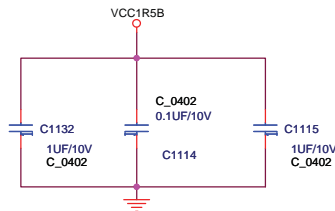
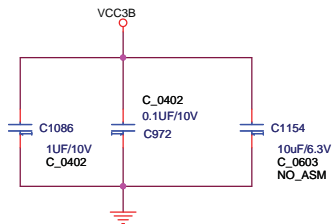
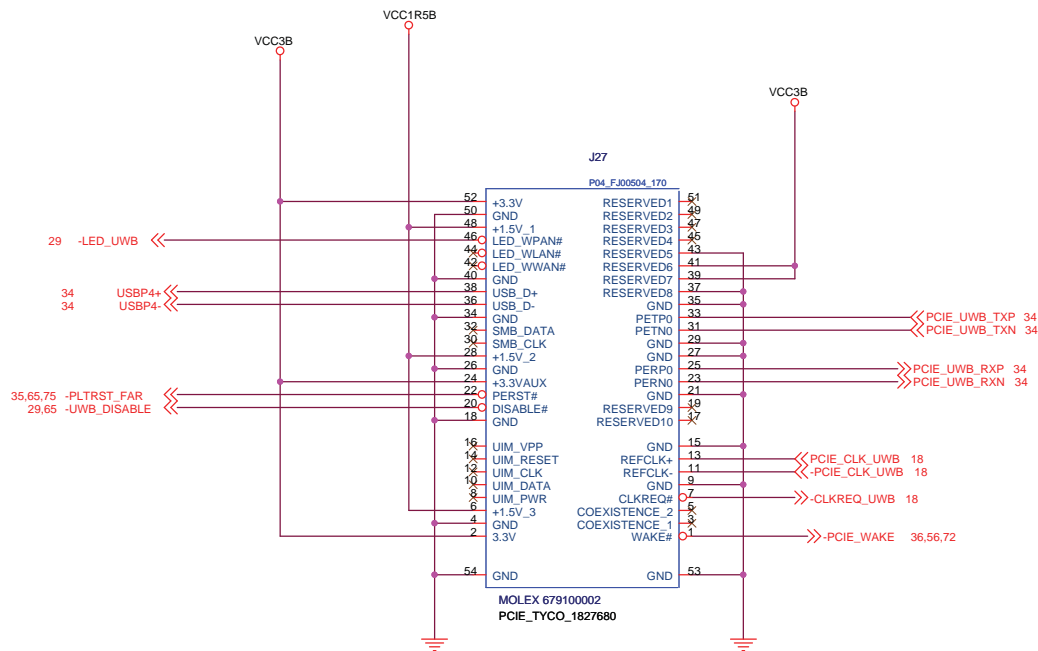
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LENOVO CONFIDENTIAL		sheet 58 of 102 Tuesday, October 30, 2007


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LENOVO CONFIDENTIAL		sheet 59 of 102 Tuesday, October 30, 2007


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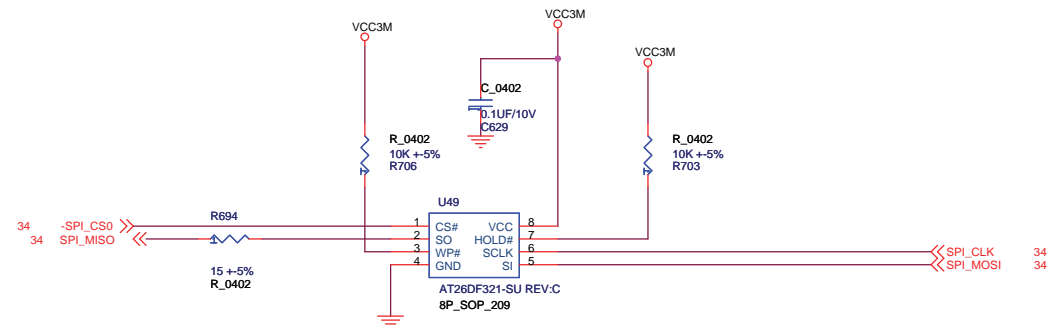
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LENOVO CONFIDENTIAL		sheet 60 of 102 Tuesday, October 30, 2007

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LENOVO CONFIDENTIAL		sheet 61 of 102 Tuesday, October 30, 2007

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LENOVO CONFIDENTIAL		sheet 62 of 102 Tuesday, October 30, 2007

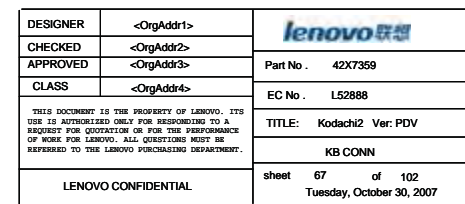


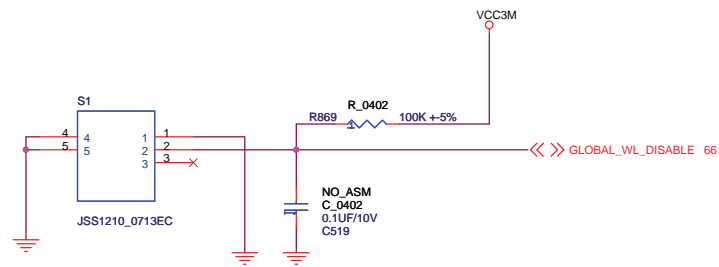
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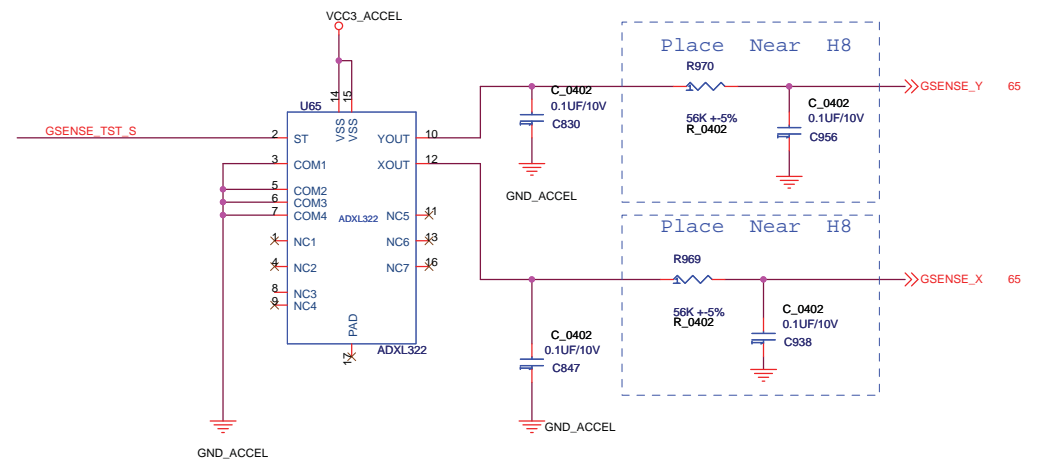
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LENOVO CONFIDENTIAL		sheet 64 of 102 Tuesday, October 30, 2007	

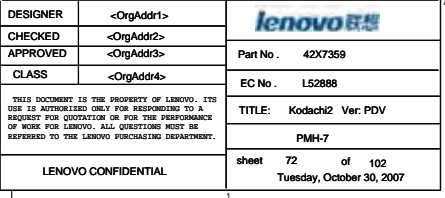





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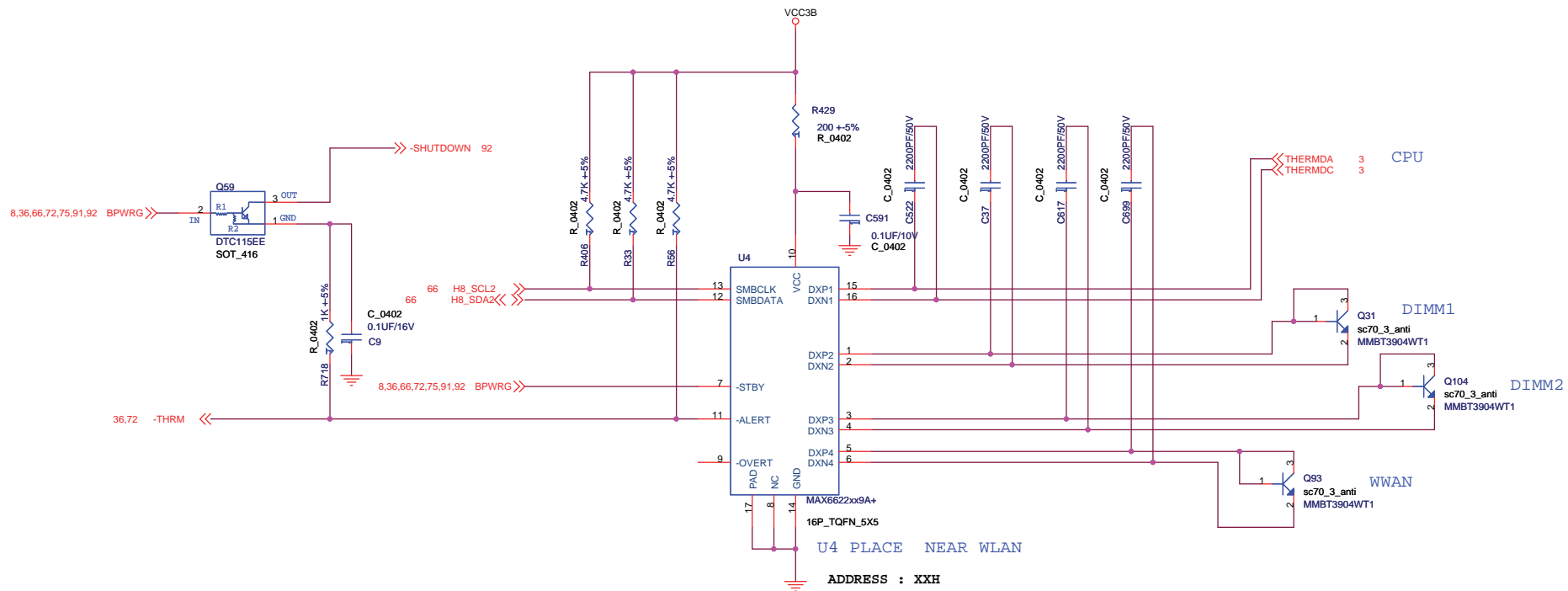


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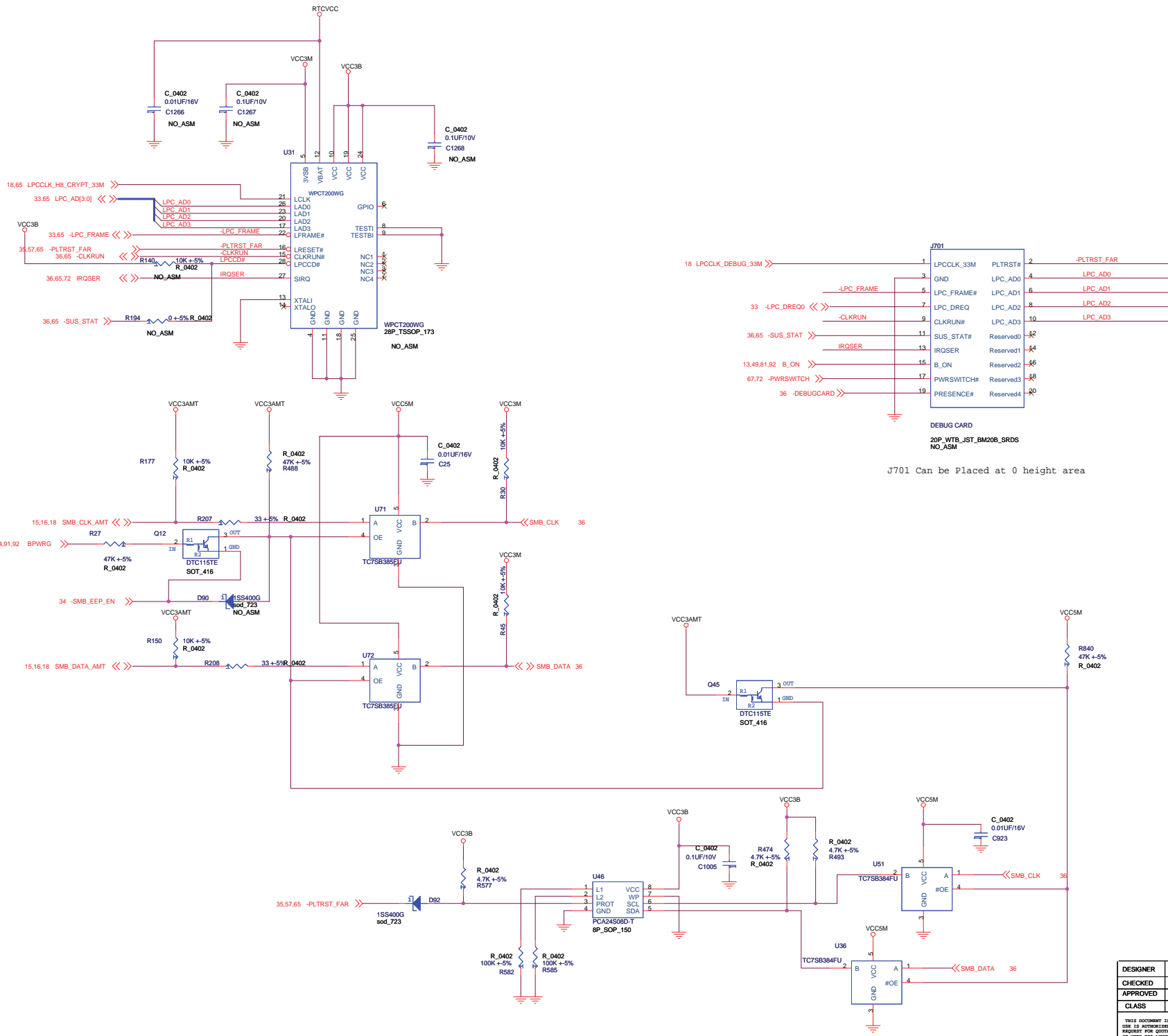



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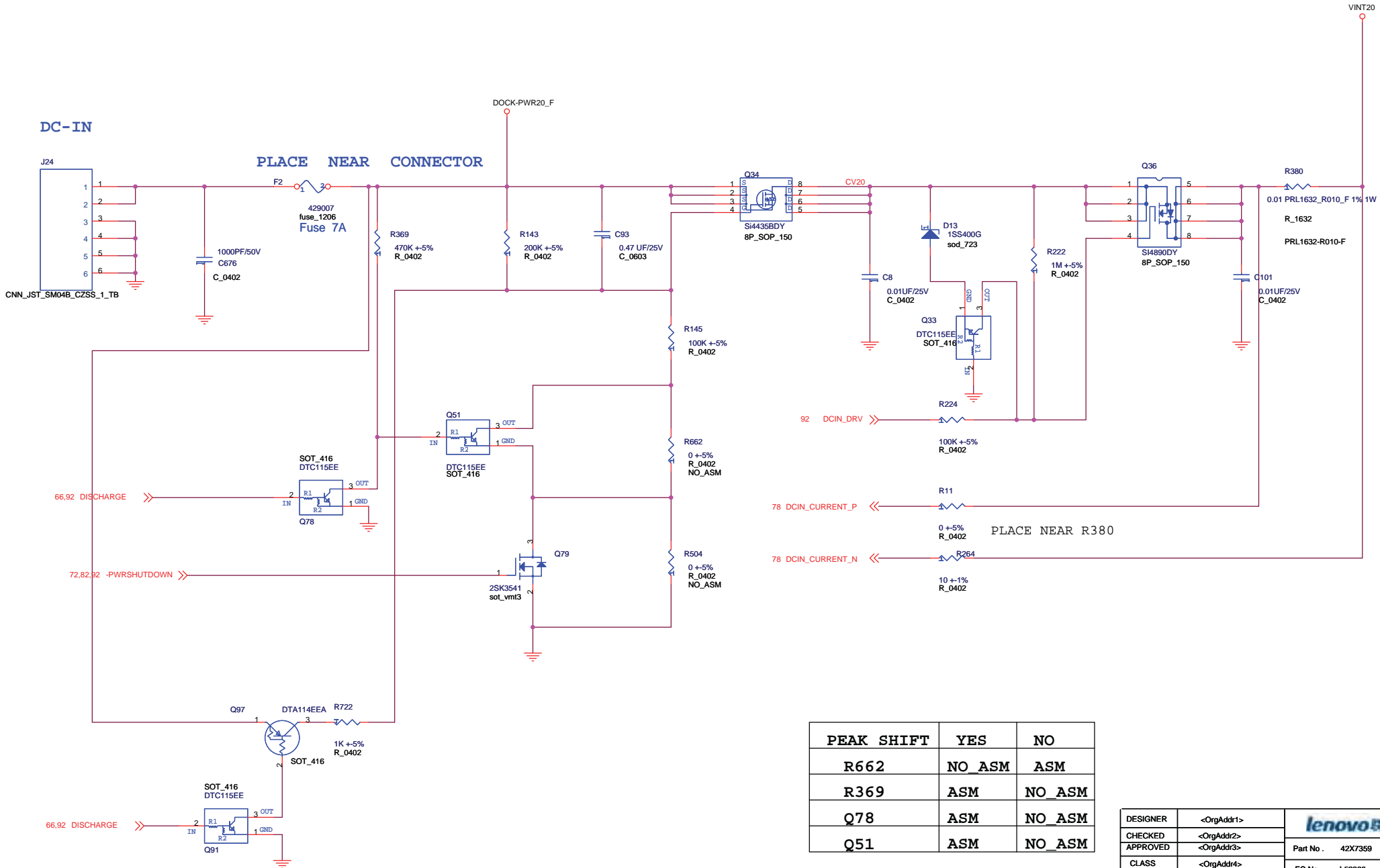
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LENOVO CONFIDENTIAL		sheet 74 of 102 Tuesday, October 30, 2007



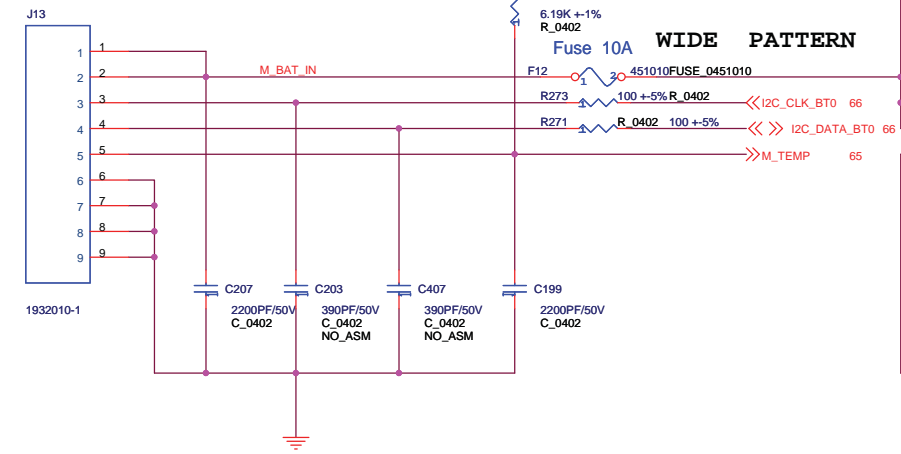
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sheet 75 of 102		
Tuesday, October 30, 2007		



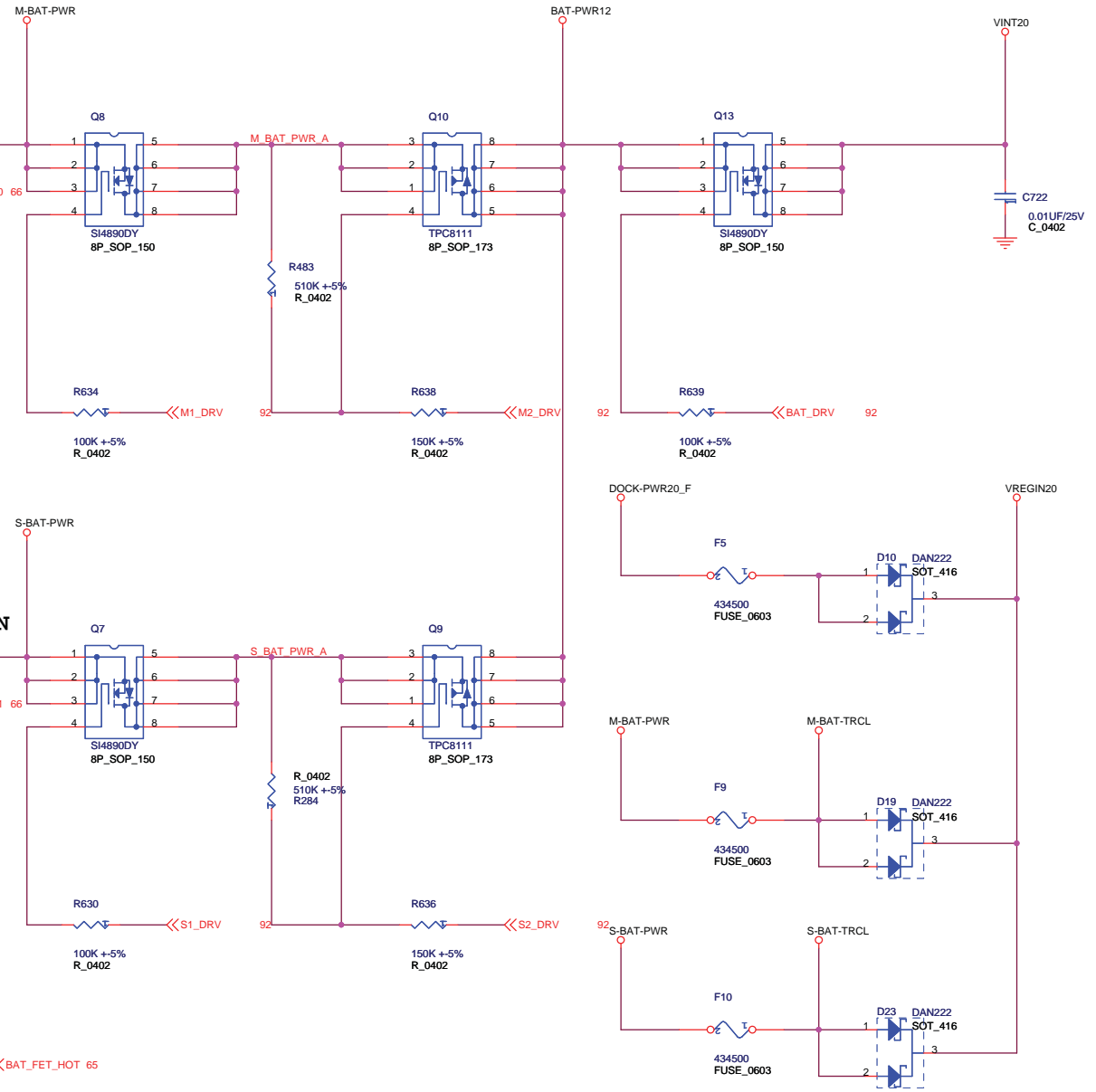
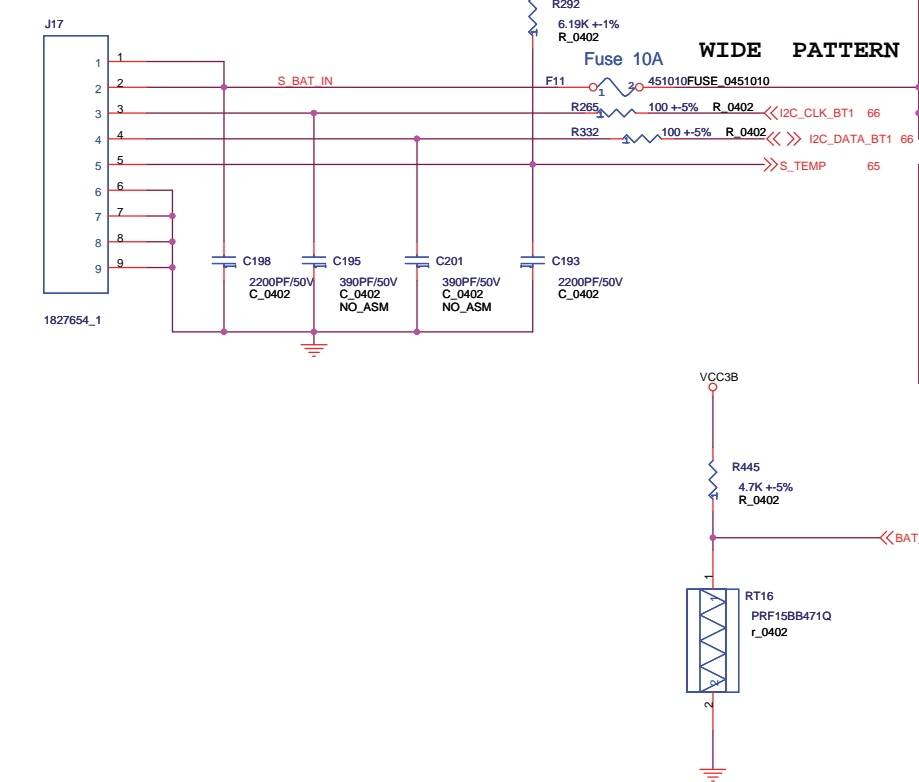
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		sheet 76 of 102 Tuesday, October 30, 2007

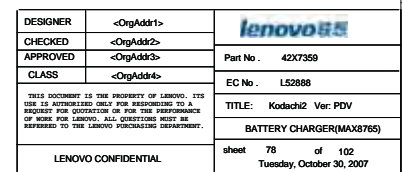
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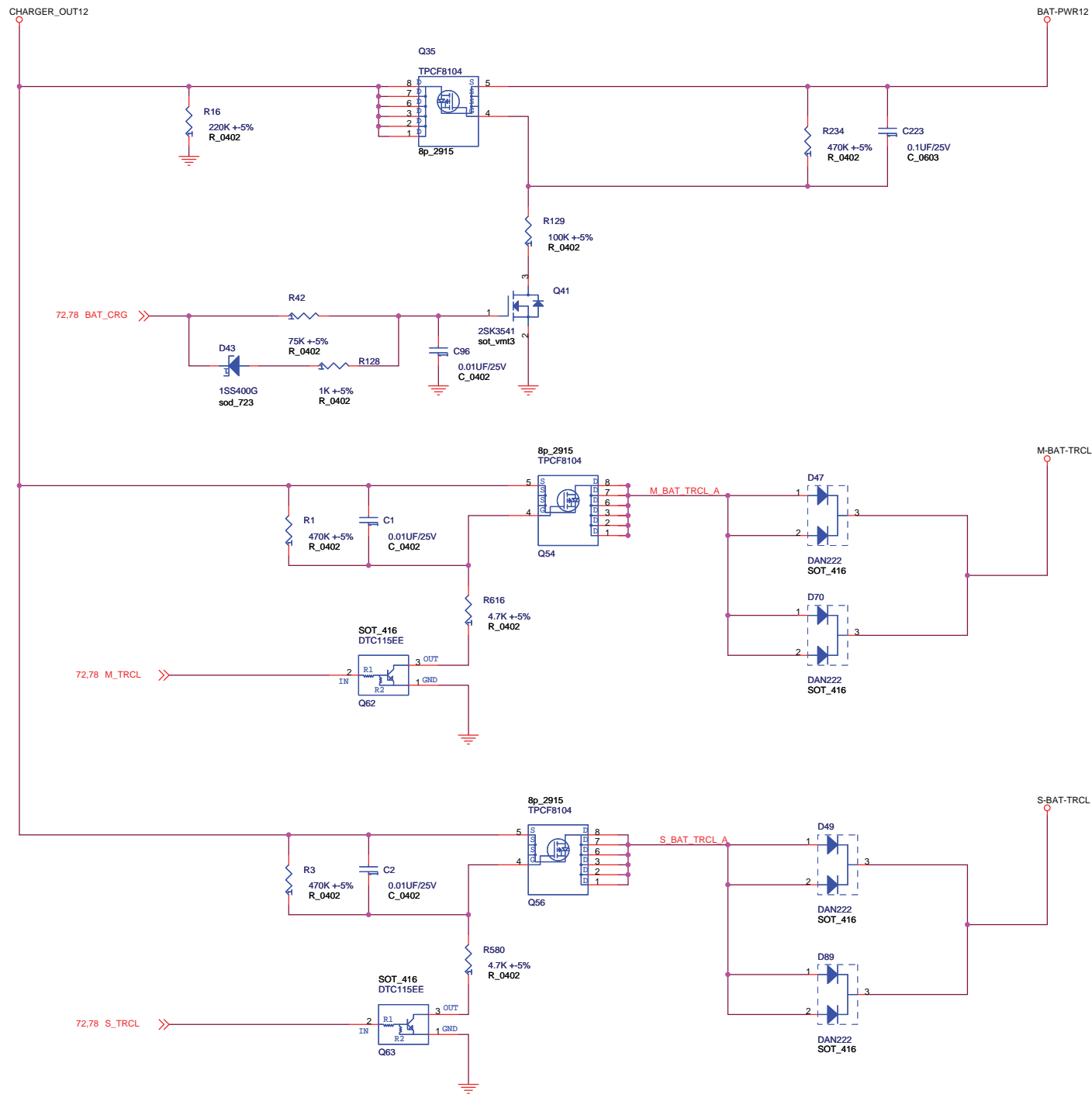



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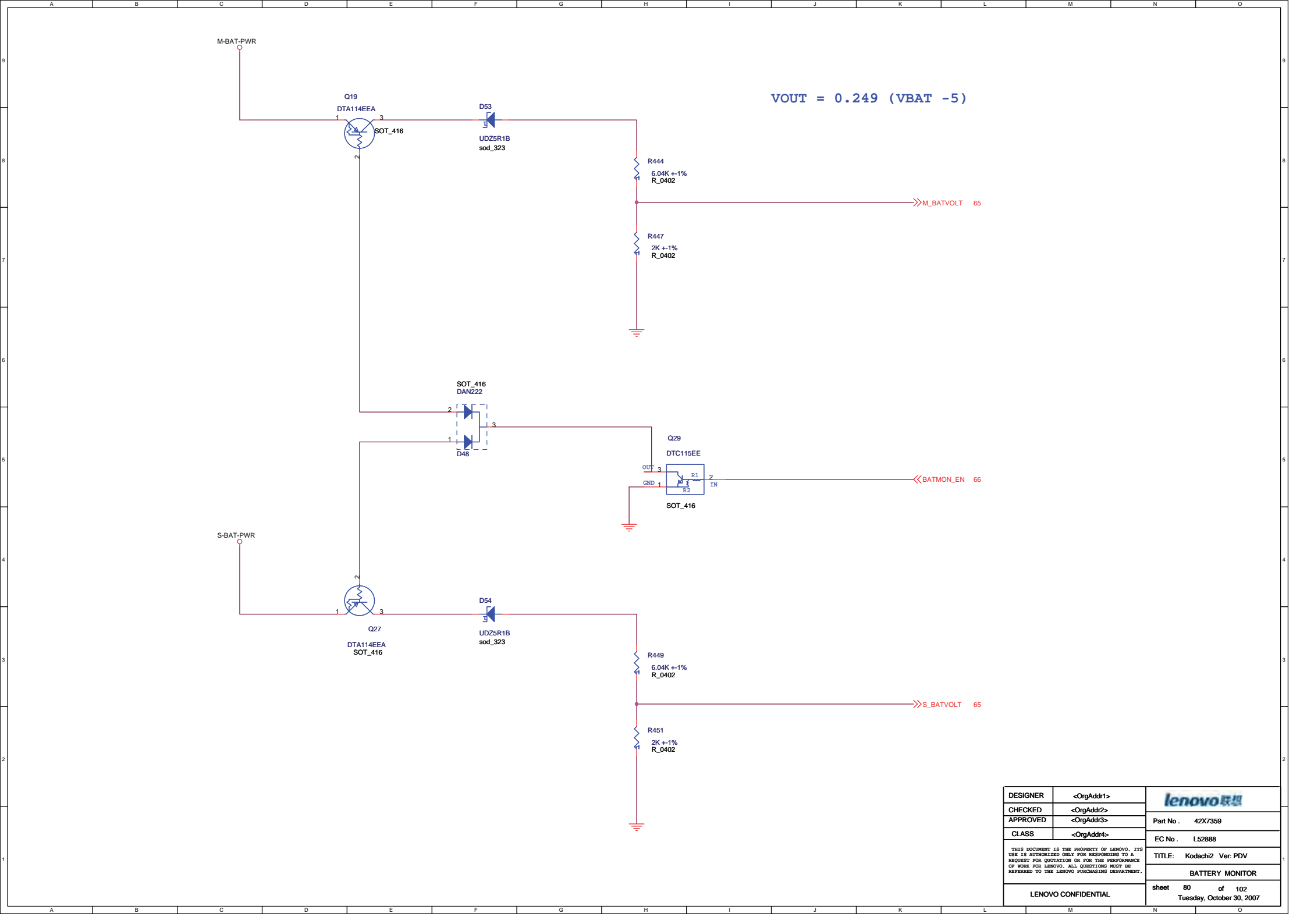


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LENOVO CONFIDENTIAL		sheet 77 of 102 Tuesday, October 30, 2007

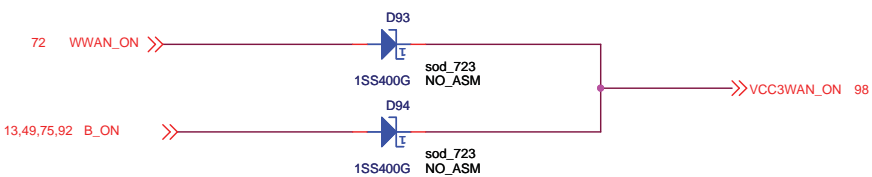
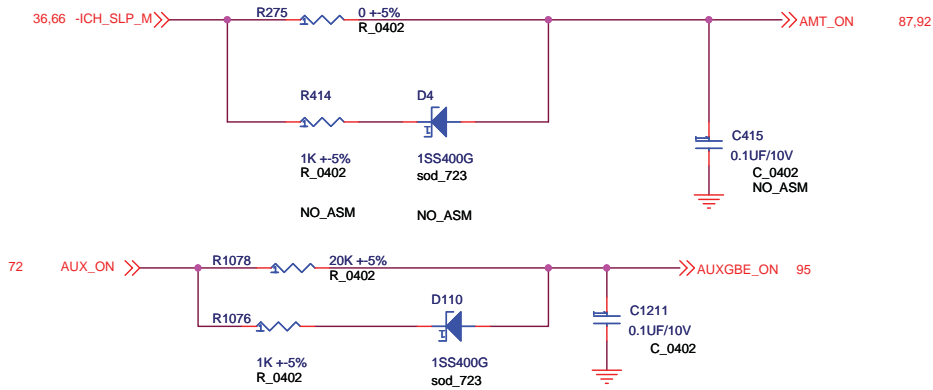
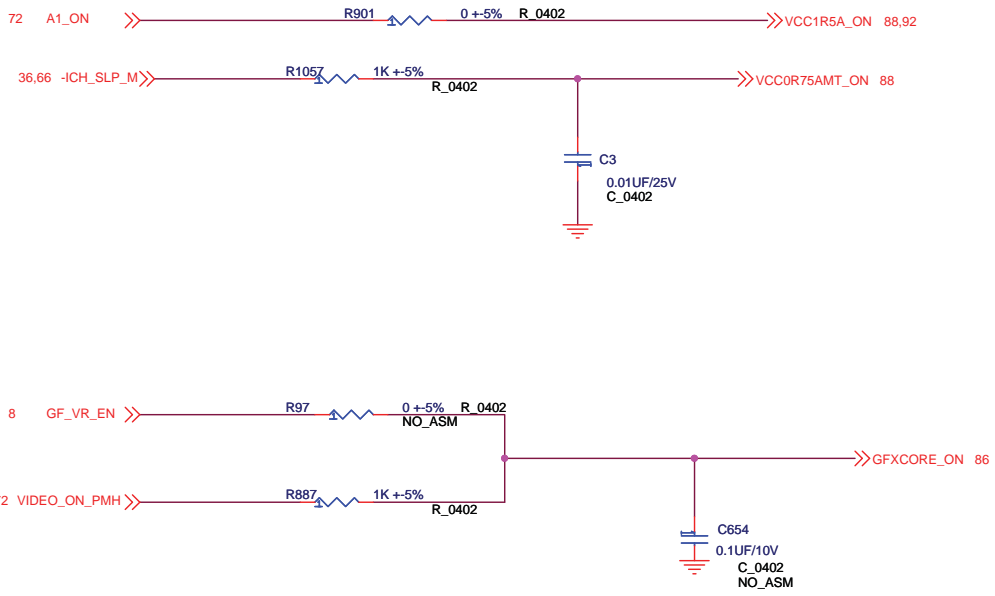
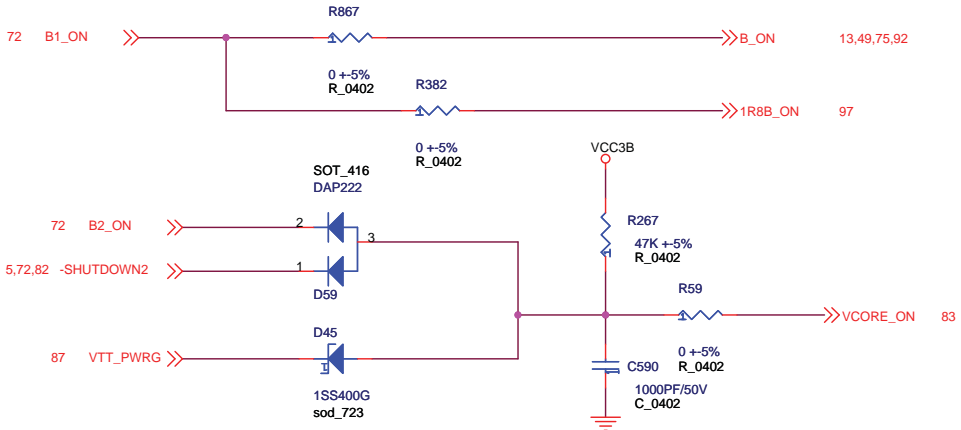
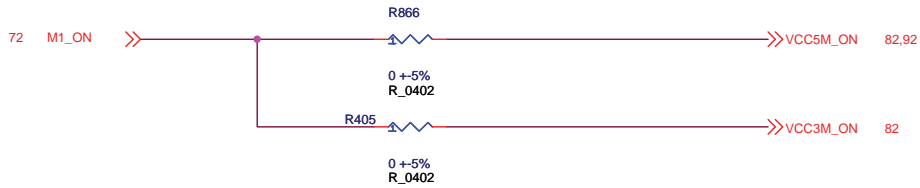





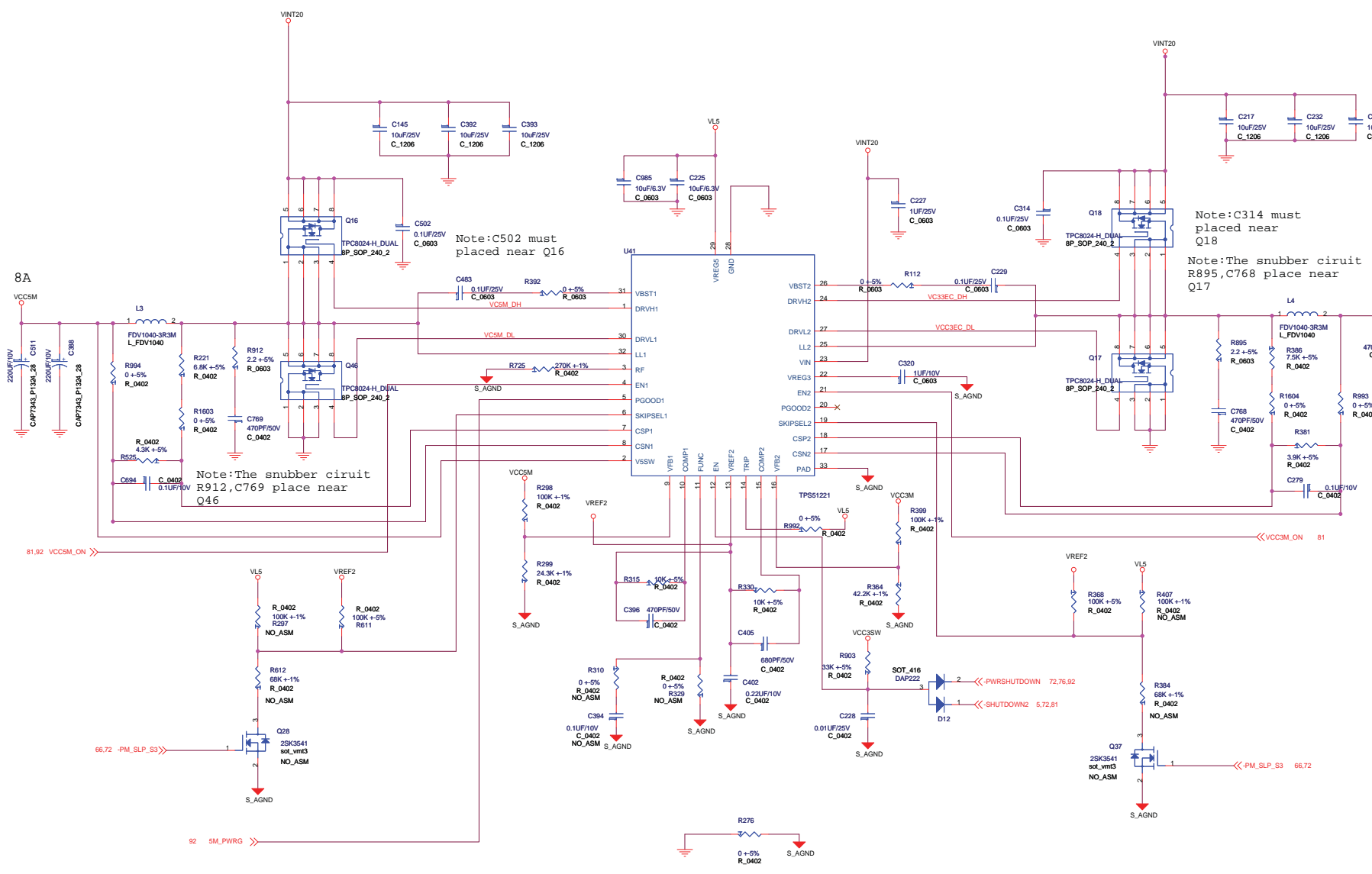
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LENOVO CONFIDENTIAL		sheet 79 of 102
		Tuesday, October 30, 2007



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		TITLE: Kodachi2 Ver: PDV
		POWER SEQUENCE
LENOVO CONFIDENTIAL		sheet 81 of 102 Tuesday, October 30, 2007




Note:C502 must placed near Q16

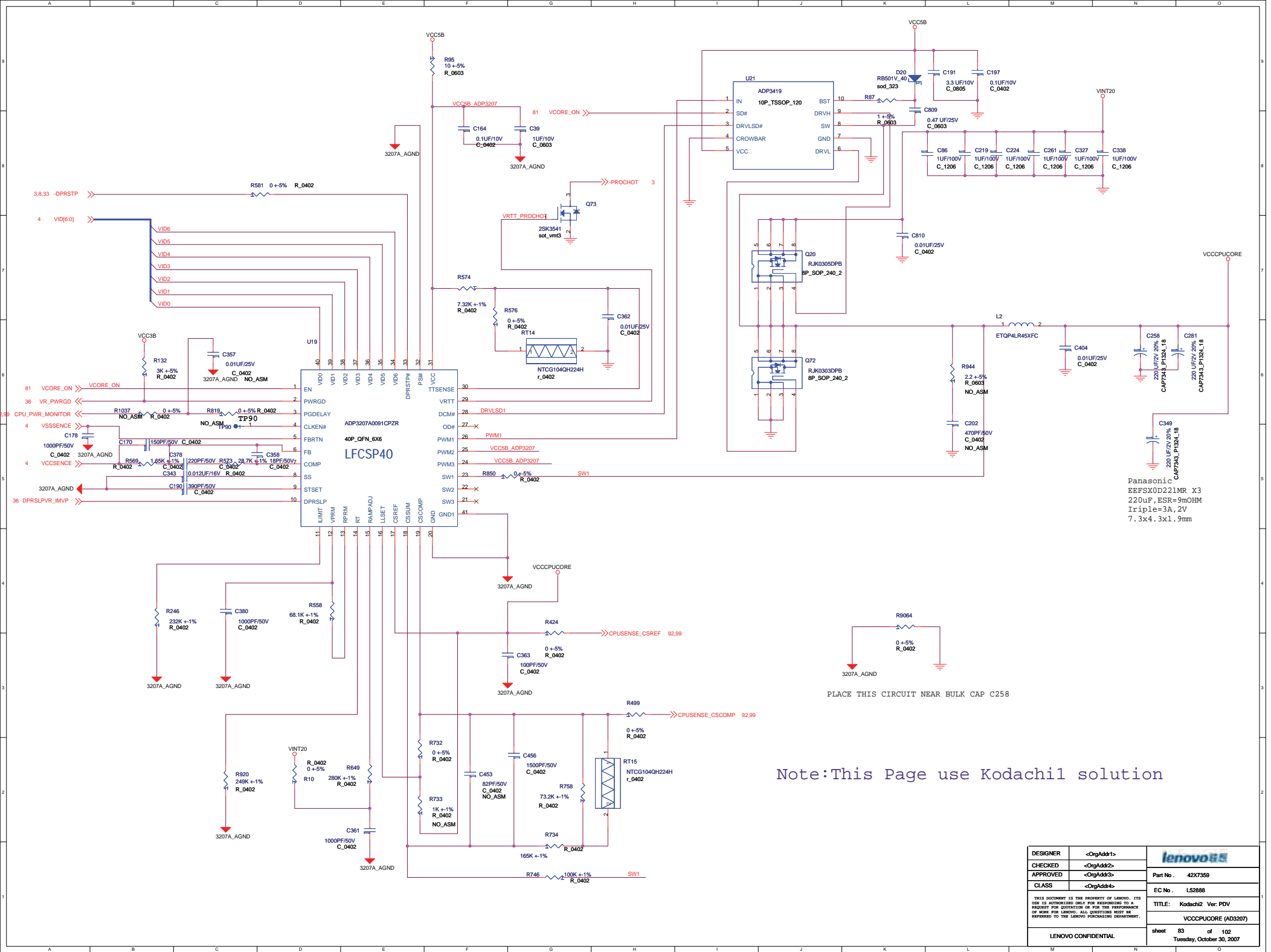
Note:C314 must placed near Q18

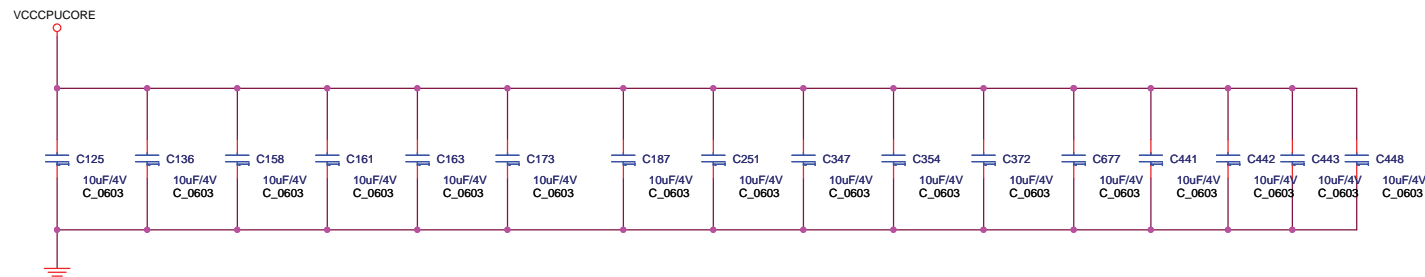
Note:The snubber circuit R895,C768 place near Q17

Note:The snubber circuit R912,C769 place near Q46

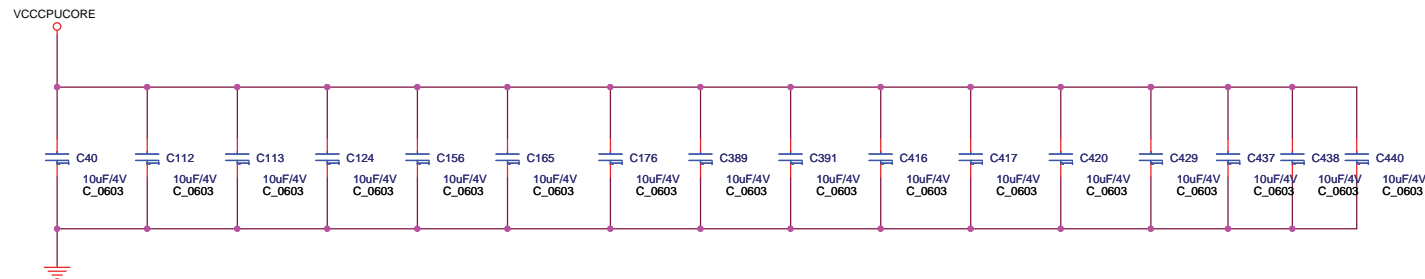
Note:R10029 must be placed near U41.28 PIN(GND)

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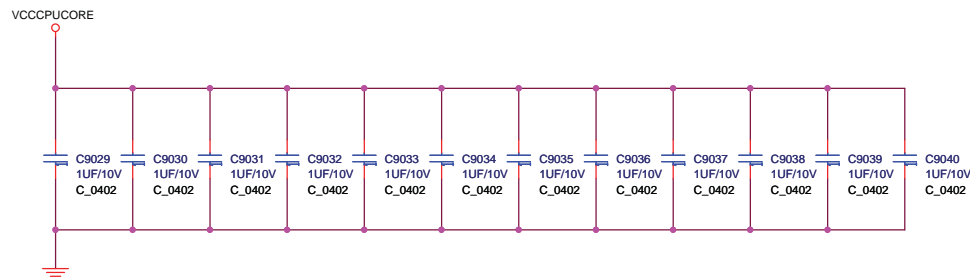




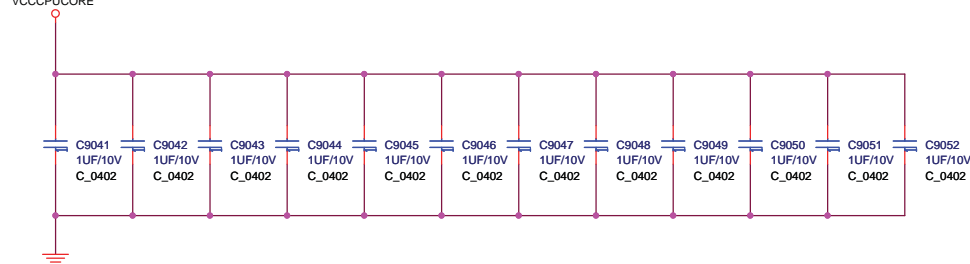
Mid-Frequency Decoupling 10UF 4V X5R 0603 1/16W X 32PCS



Note:This Page use Kodachil solution




High-Frequency Decoupling 1UF 10V X5R 0402 1/16W X 24PCS



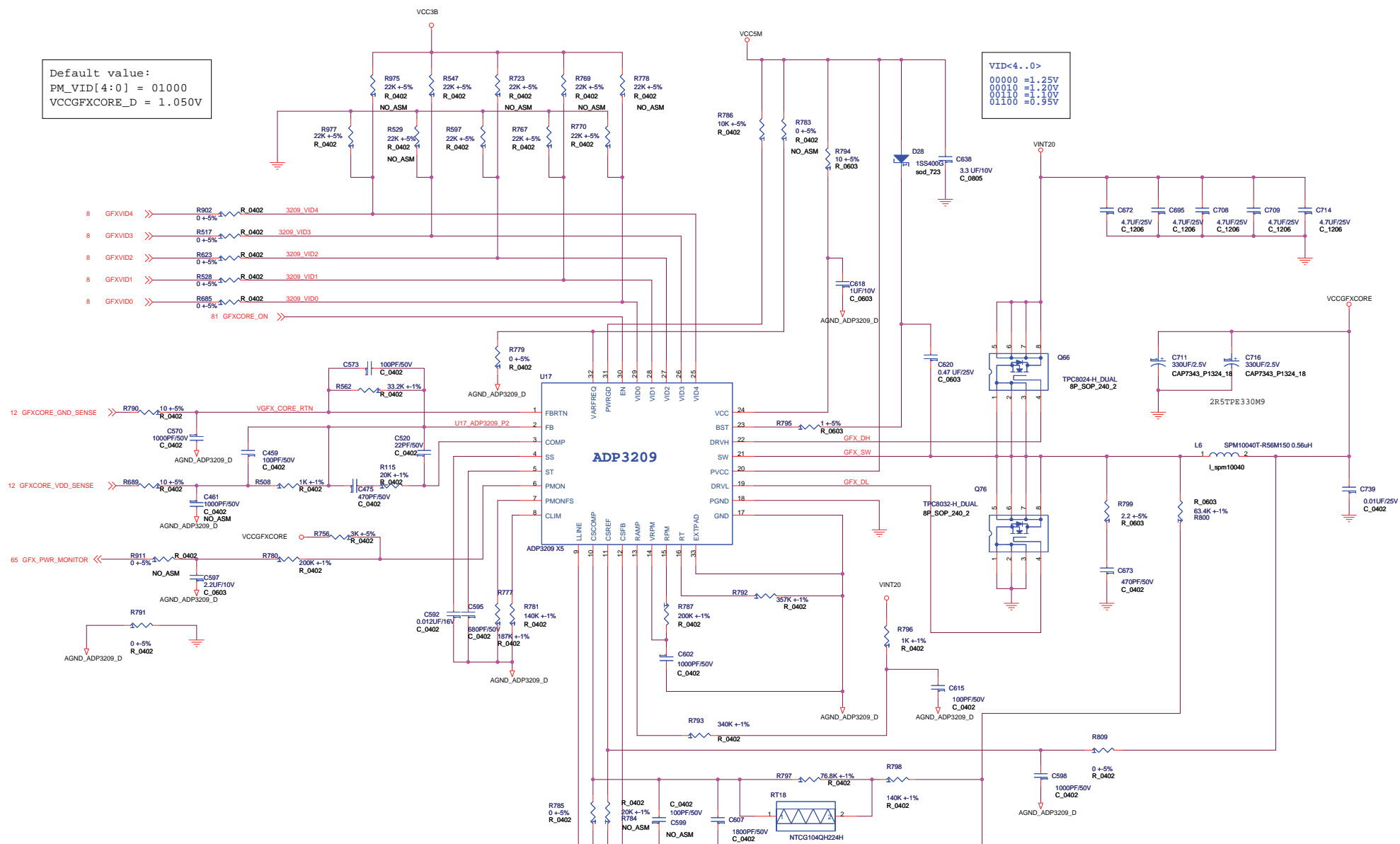
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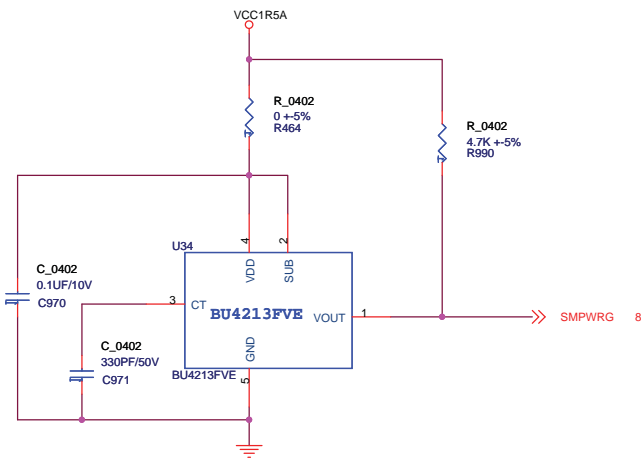
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		sheet 85 of 102 Tuesday, October 30, 2007
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VCCGFXCORE_D = 1.050V


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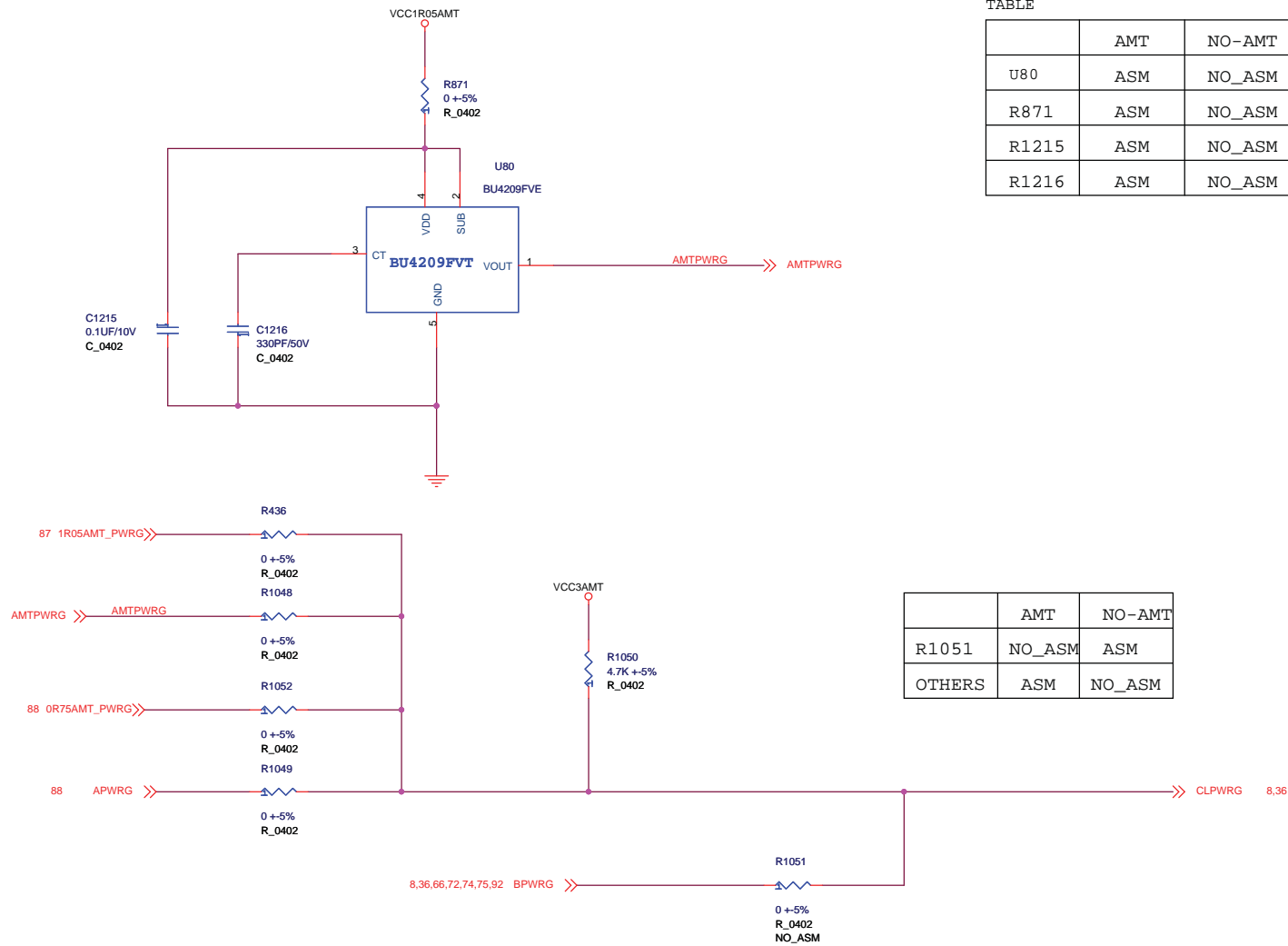




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LENOVO CONFIDENTIAL		LOAD SW VIDEO
		sheet 90 of 102 Tuesday, October 30, 2007

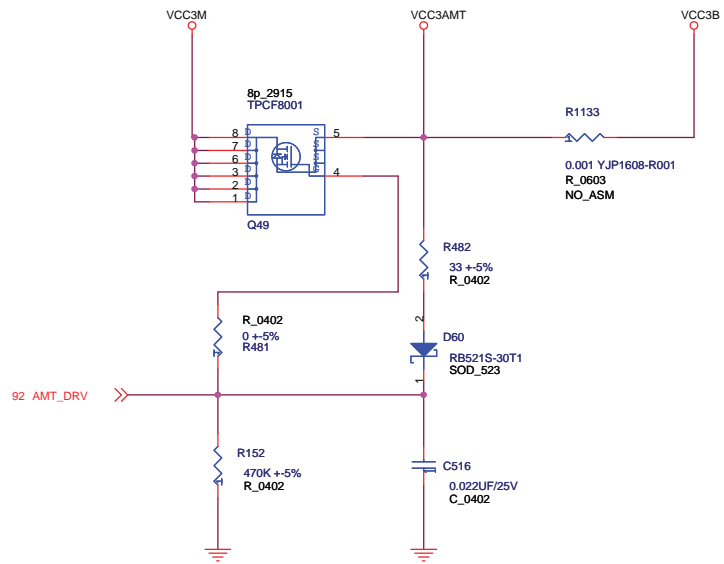
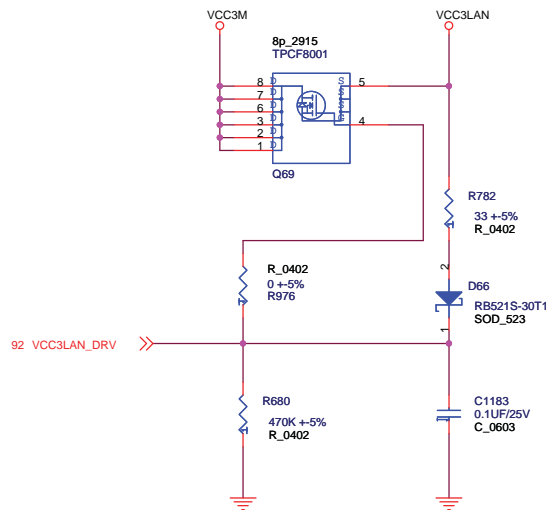
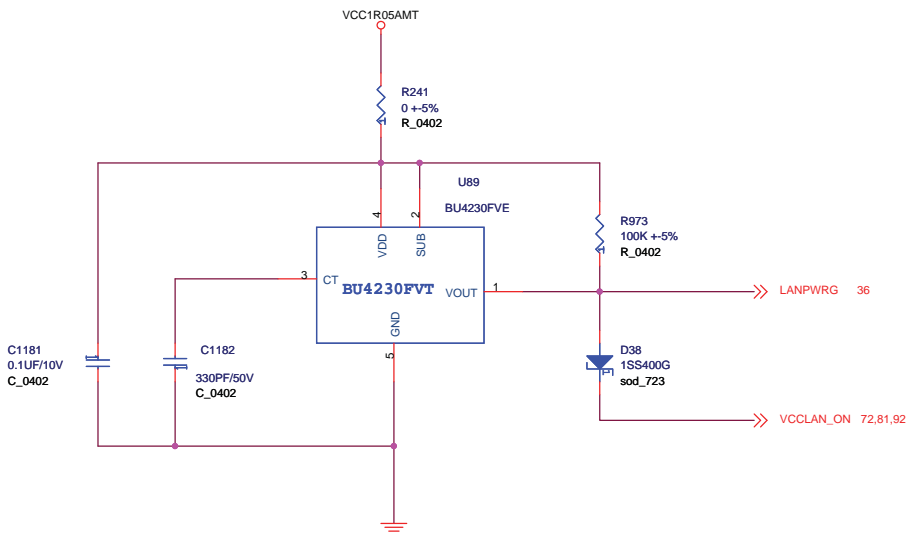


TABLE

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U80	ASM	NO_ASM
R871	ASM	NO_ASM
R1215	ASM	NO_ASM
R1216	ASM	NO_ASM


	AMT	NO-AMT
R1051	NO_ASM	ASM
OTHERS	ASM	NO_ASM

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		TITLE: Kodachi2 Ver: PDV
		CLPWRG
LENOVO CONFIDENTIAL		sheet 91 of 102 Tuesday, October 30, 2007




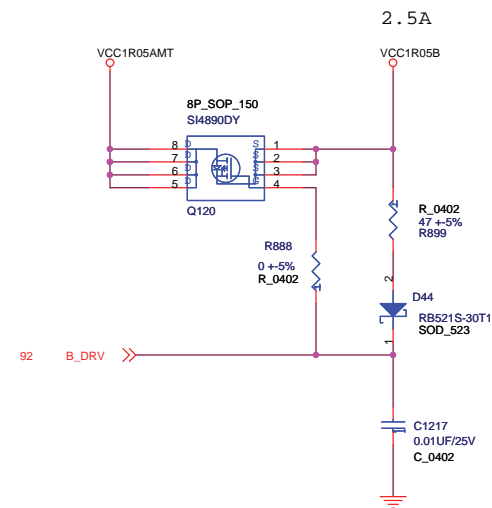
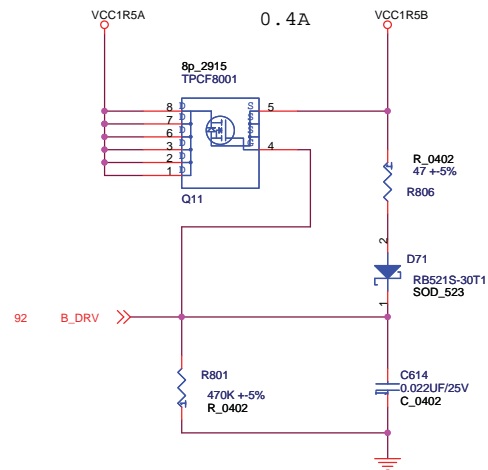
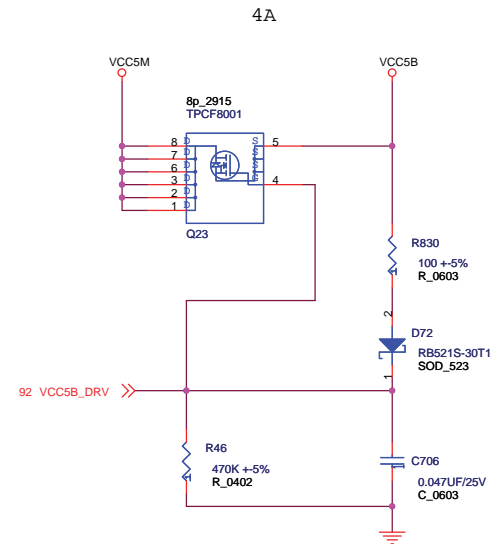
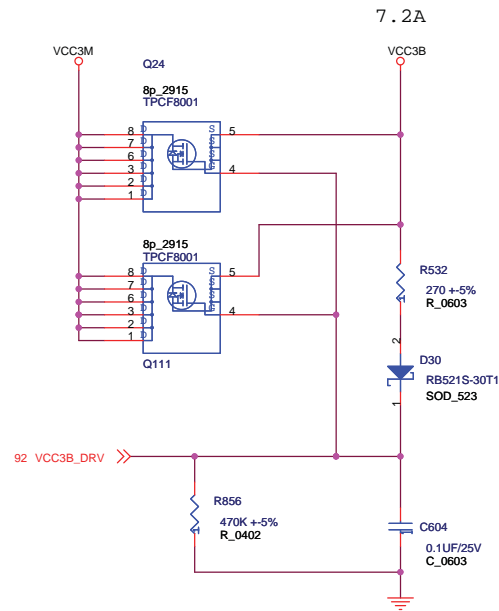
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R1133	NO_ASM	ASM
THE OTHER	ASM	NO_ASM

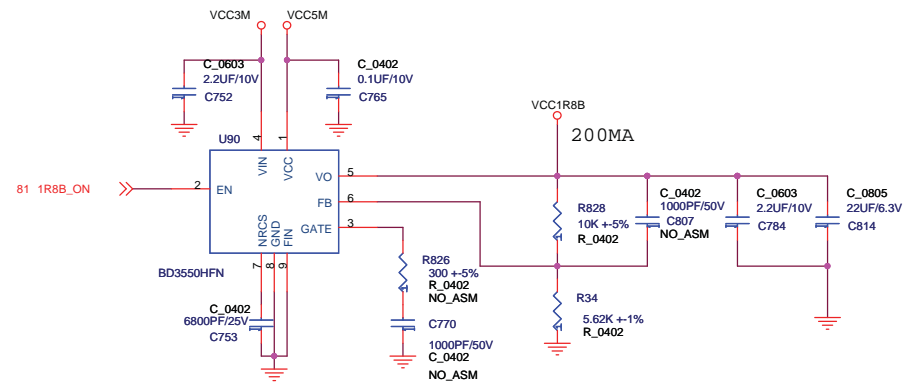
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		EC No . L52888
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		LOAD SW AMT&LAN
LENOVO CONFIDENTIAL		sheet 93 of 102 Tuesday, October 30, 2007


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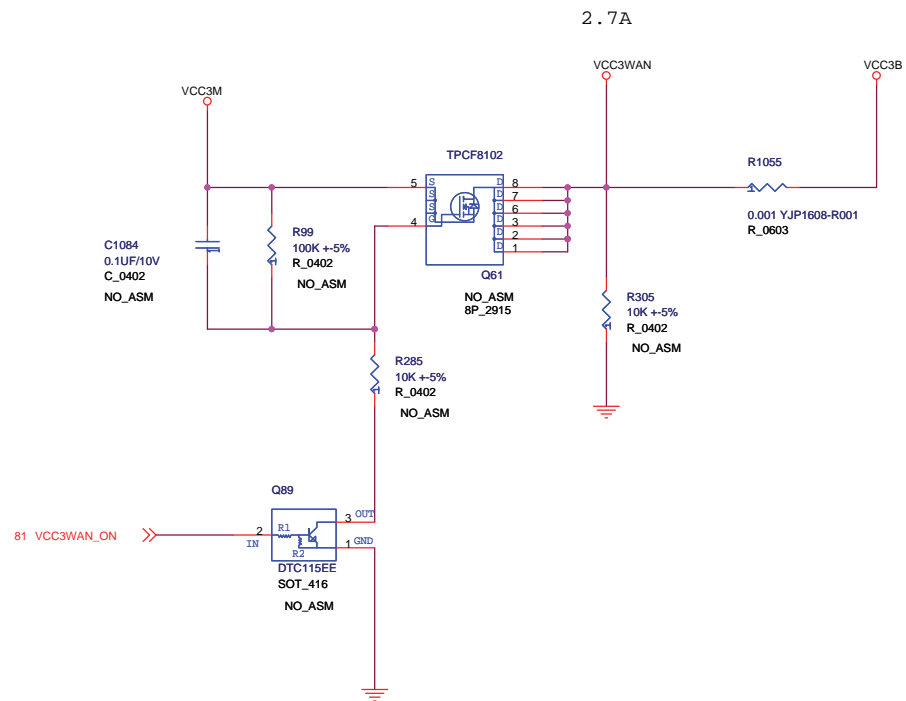
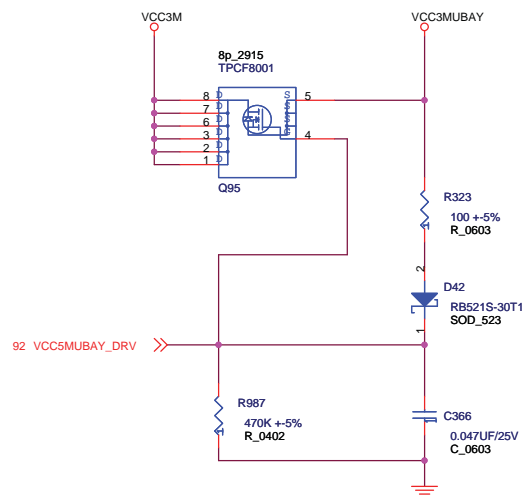
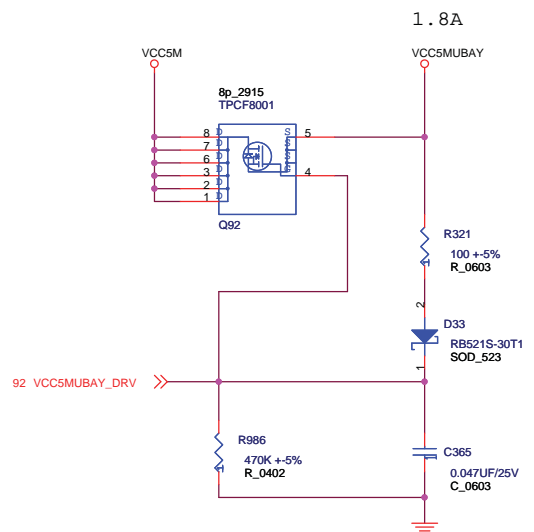
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LENOVO CONFIDENTIAL		LOAD SW VEDIO
		sheet 94 of 102 Tuesday, October 30, 2007




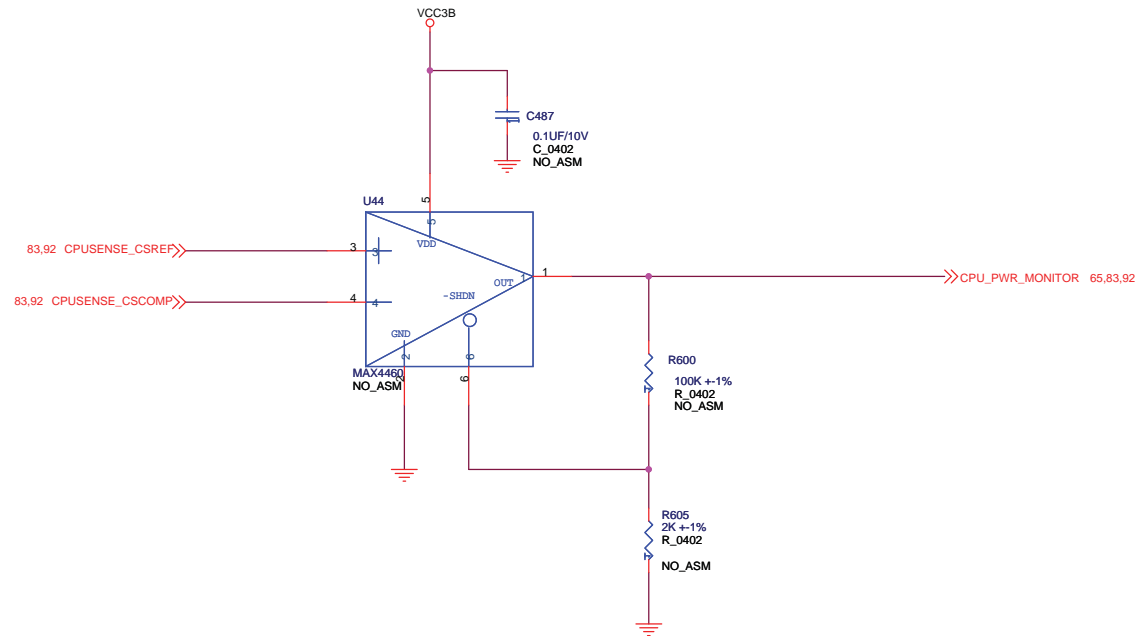
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		TITLE: Kodachi2 Ver: PDV
		LOAD SW B
LENOVO CONFIDENTIAL		sheet 96 of 102 Tuesday, October 30, 2007



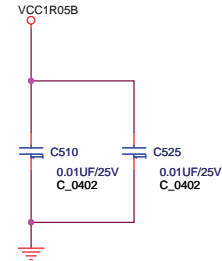
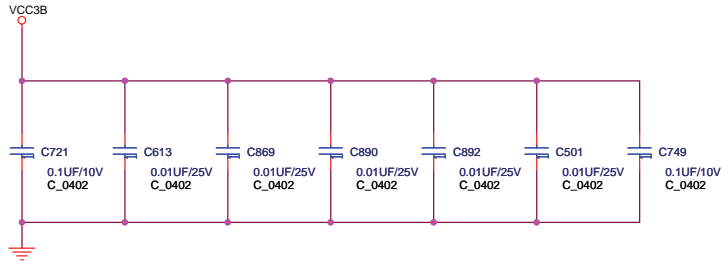
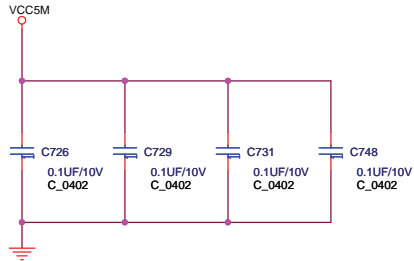
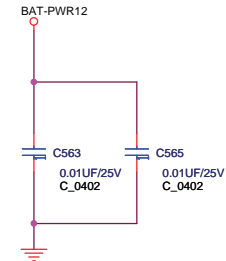
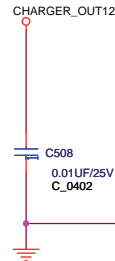
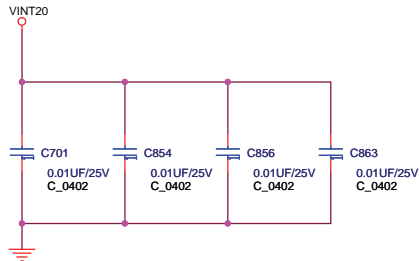
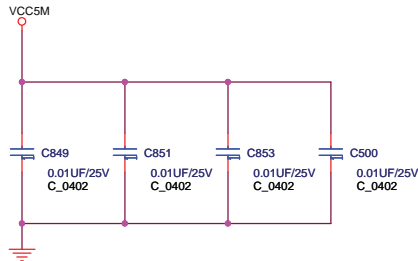
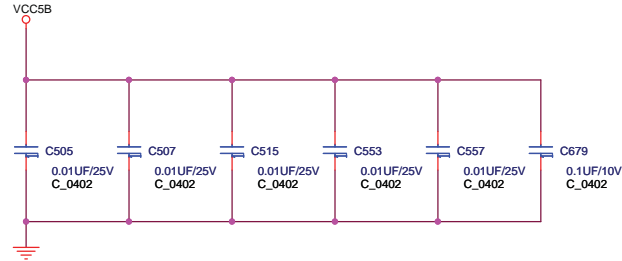
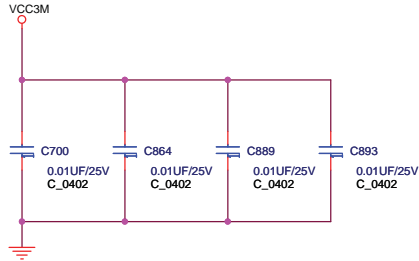
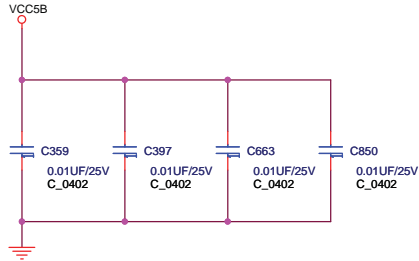
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LENOVO CONFIDENTIAL		sheet 97 of 102 Tuesday, October 30, 2007




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		LOAD SW UBAY&WWAN
LENOVO CONFIDENTIAL		sheet 98 of 102 Tuesday, October 30, 2007

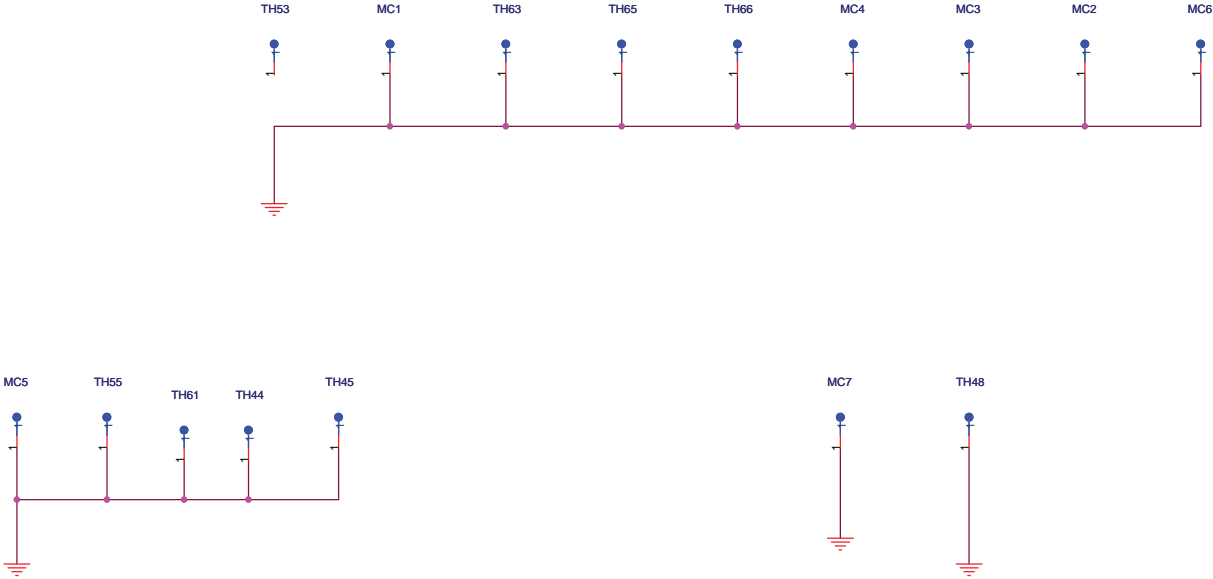


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PTH FOR SCREW HOLE




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KODACHI2 EC History List

Pre DV phase--->

Ver.1.00	20070822	APPLIED	KODACHI2_SDV_EC	001-004
Ver.1.01	20070906	APPLIED	KODACHI2_SDV_EC	005-009
Ver.1.02	20070915	APPLIED	KODACHI2_SDV_EC	010-020
Ver.1.03	20070918	APPLIED	KODACHI2_SDV_EC	021-027
Ver.1.04	20070920	APPLIED	KODACHI2_SDV_EC	028-031
Ver.1.05	20070921	APPLIED	KODACHI2_SDV_EC	032-035
Ver.1.06	20070924	APPLIED	KODACHI2_SDV_EC	036-039
Ver.1.07	20070924	APPLIED	KODACHI2_SDV_EC	040-044
Ver.1.08	20071009	APPLIED	KODACHI2_SDV_EC	045-055
Ver.1.09	20071017	APPLIED	KODACHI2_SDV_EC	056-065
Ver.1.10	20071019	APPLIED	KODACHI2_SDV_EC	066-077
Ver.1.11	20071024	APPLIED	KODACHI2_SDV_EC	078-086

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LENOVO CONFIDENTIAL		sheet 102 of 102 Tuesday, October 30, 2007